

FIG. 1
PRIOR ART

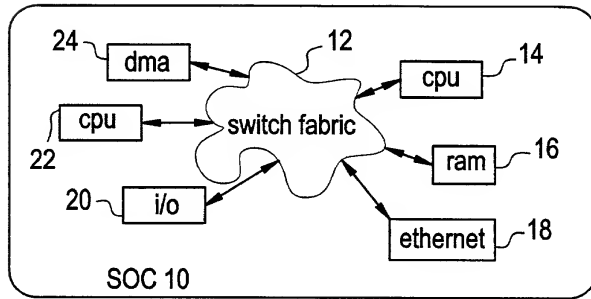
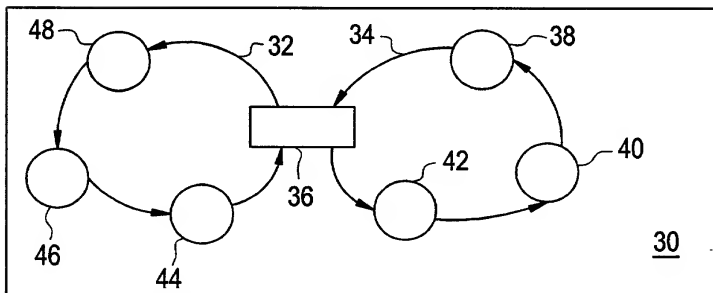


FIG. 2



201250" EP-29001

FIG. 3

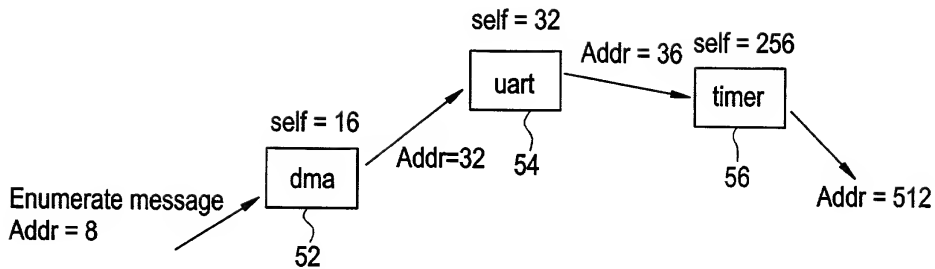


FIG. 4

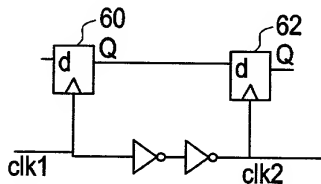
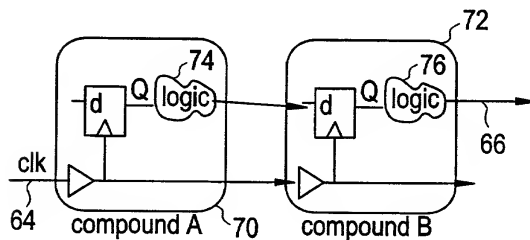


FIG. 5



204260" ETE-4900T

FIG. 6

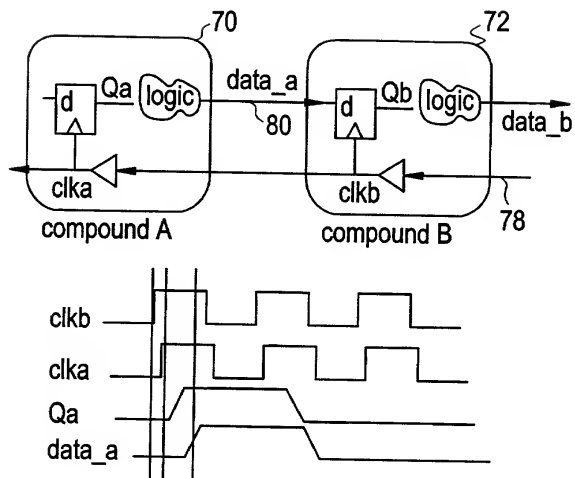


FIG. 7

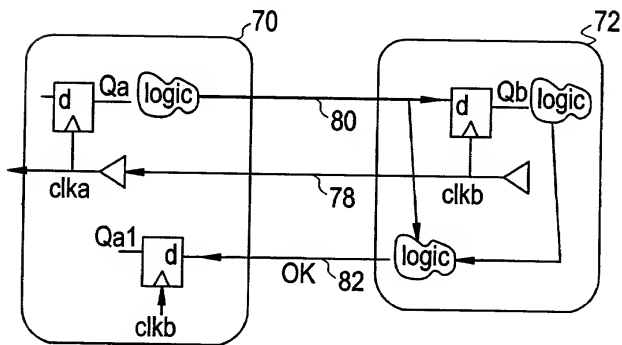


FIG. 8

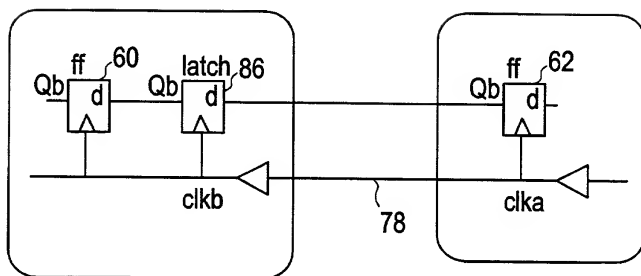


FIG. 9

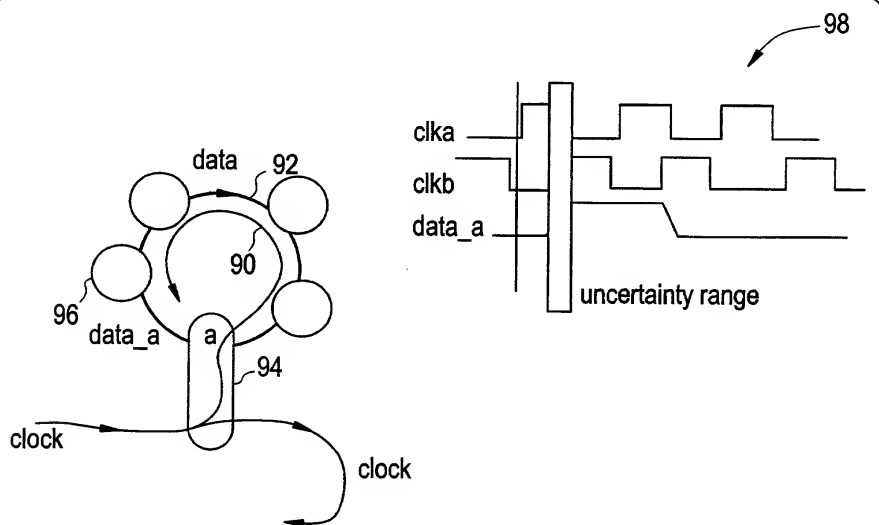


FIG. 11

local_clock — 110

data_in

local_data_out

114

data from previous member

clk

112

116

data

clock

FIG. 14

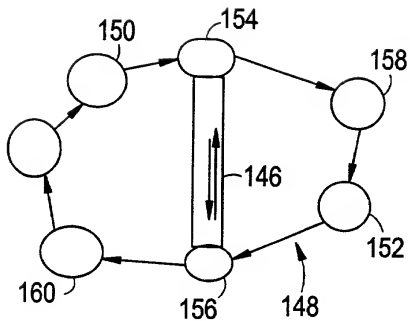


FIG. 15

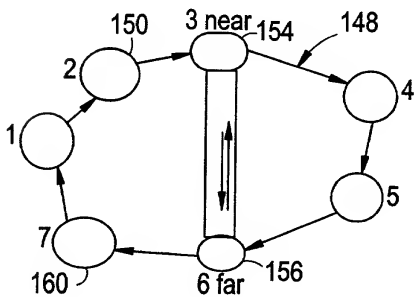


FIG. 16

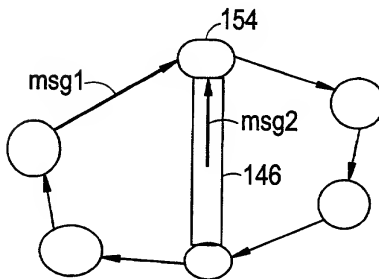


FIG. 17

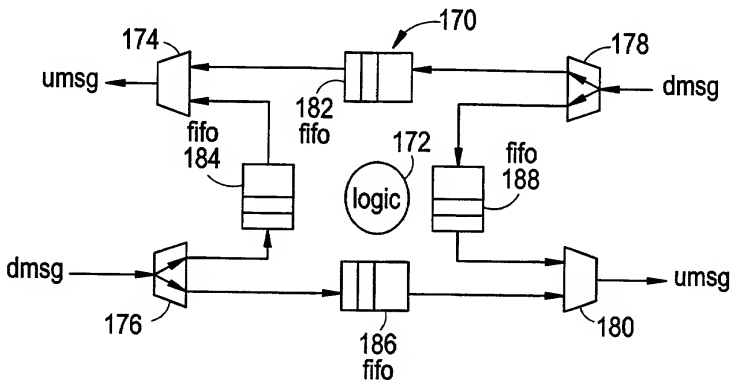


FIG. 18

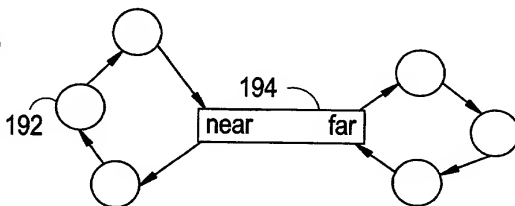


FIG. 19

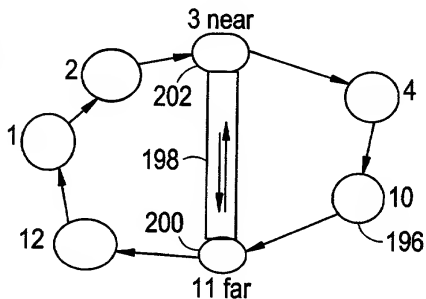


FIG. 20

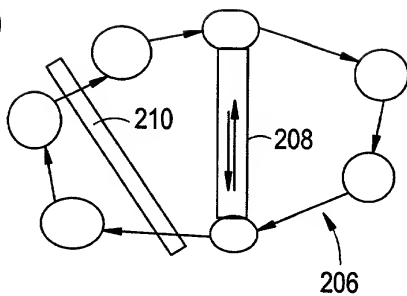


FIG. 21

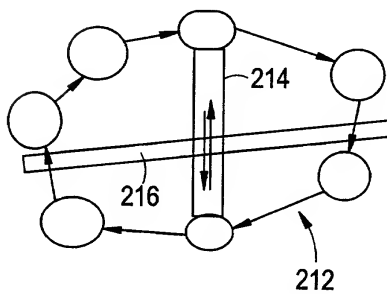


FIG. 22

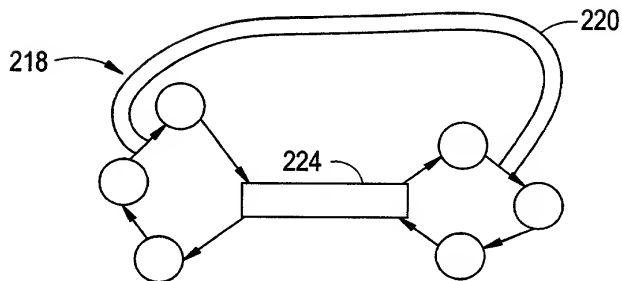


FIG. 23

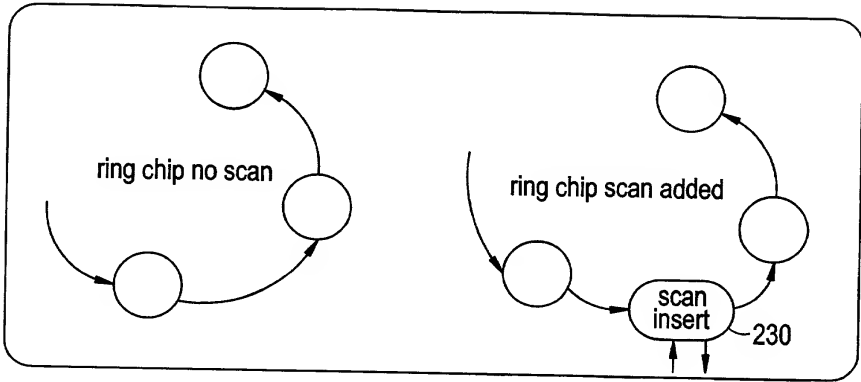


FIG. 24

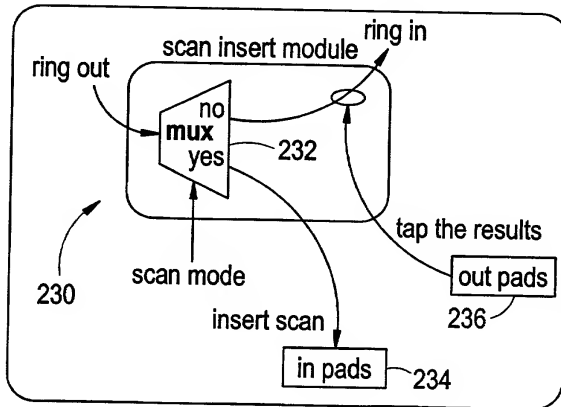


FIG. 25

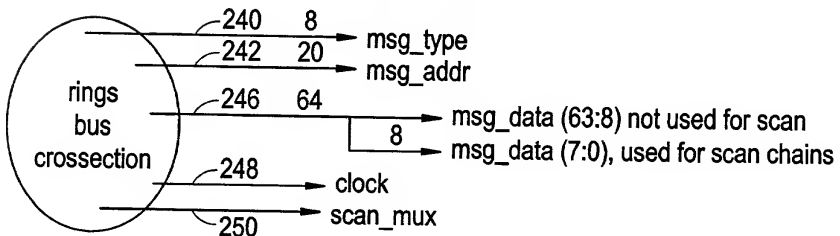


FIG. 26

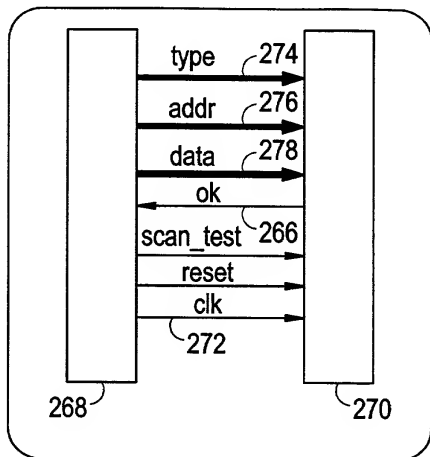


FIG. 27

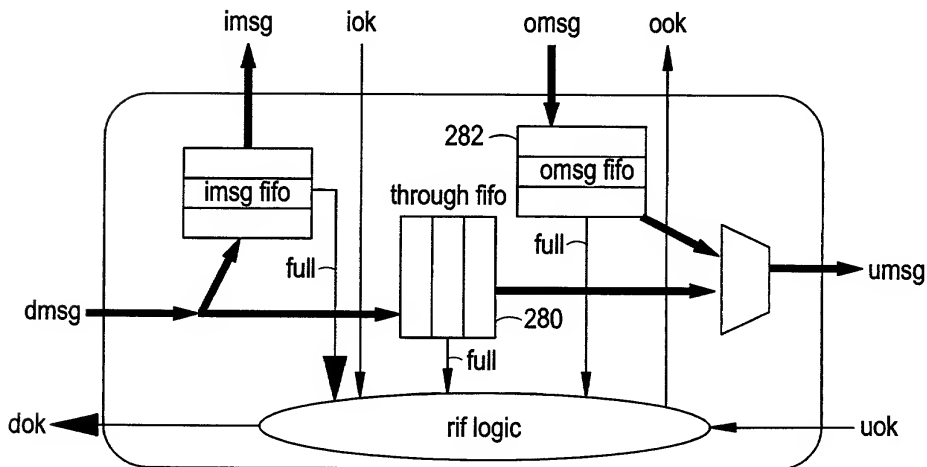


FIG. 28

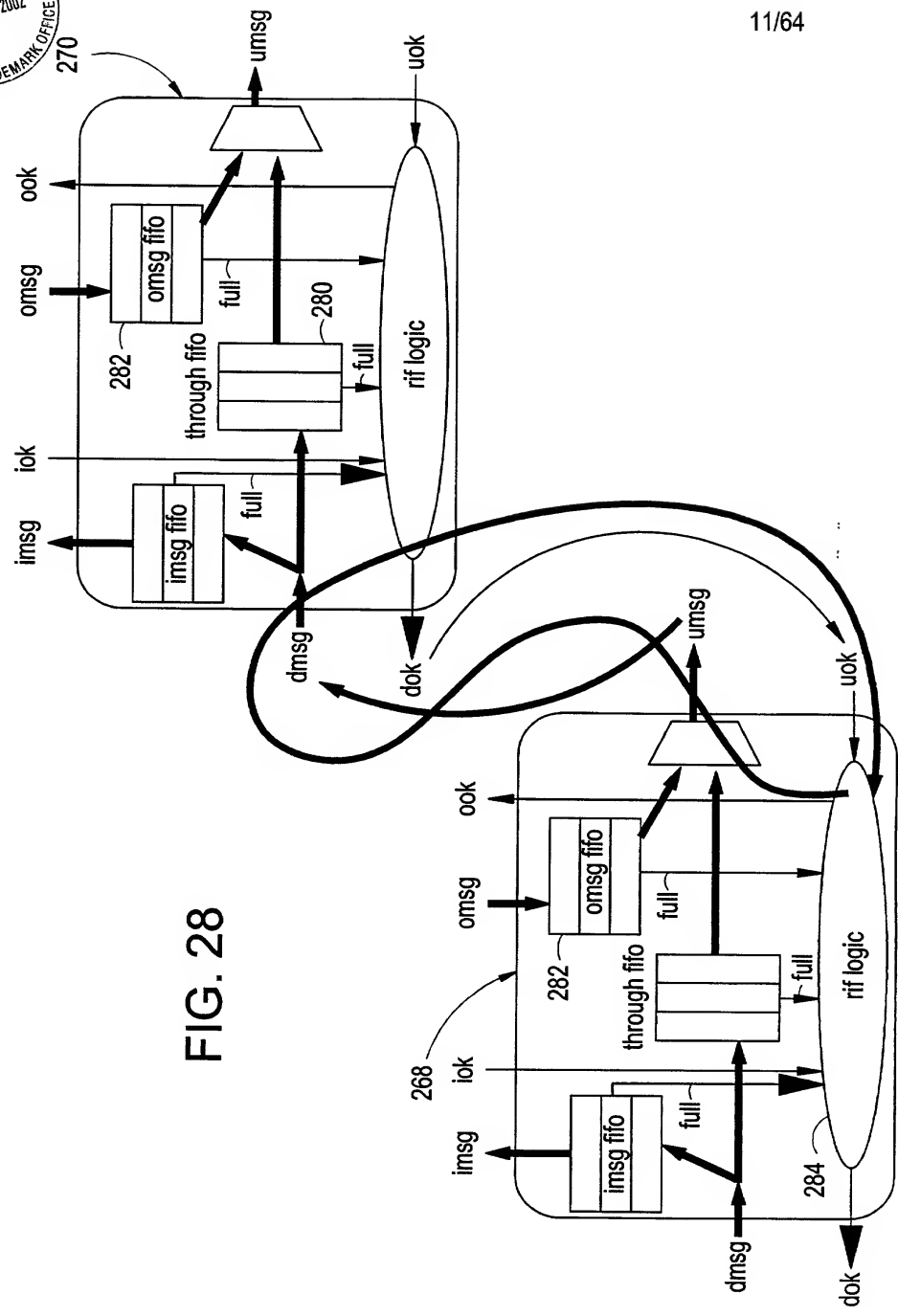


FIG. 29

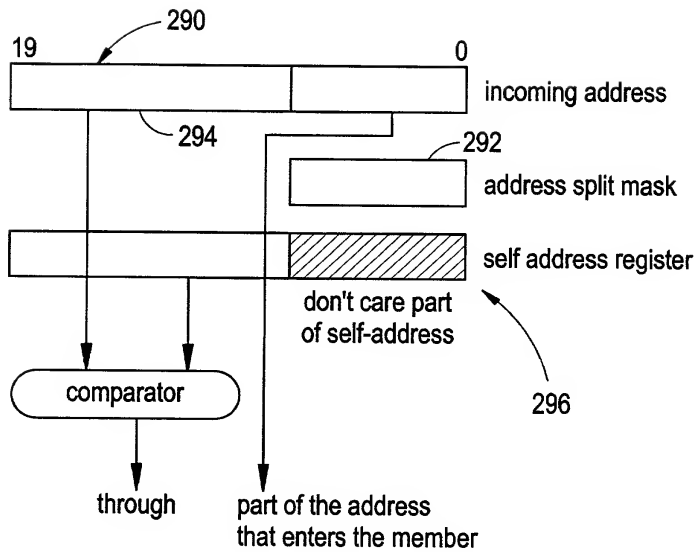


FIG. 30

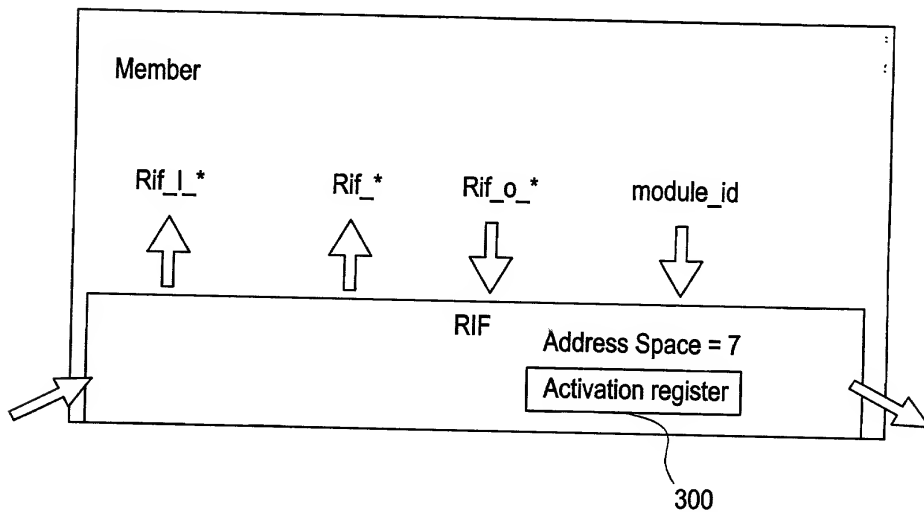


FIG. 31

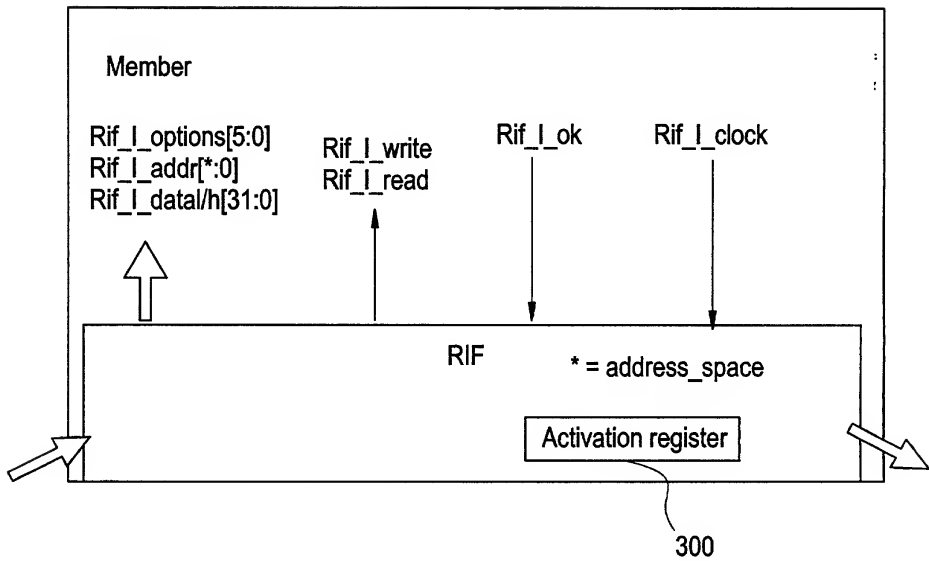


FIG. 32

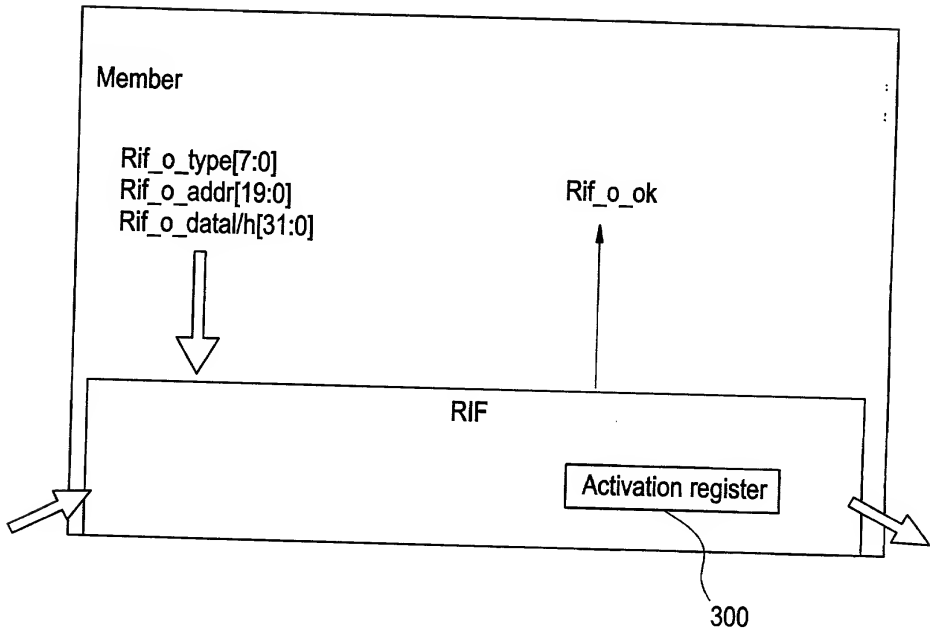


FIG. 33

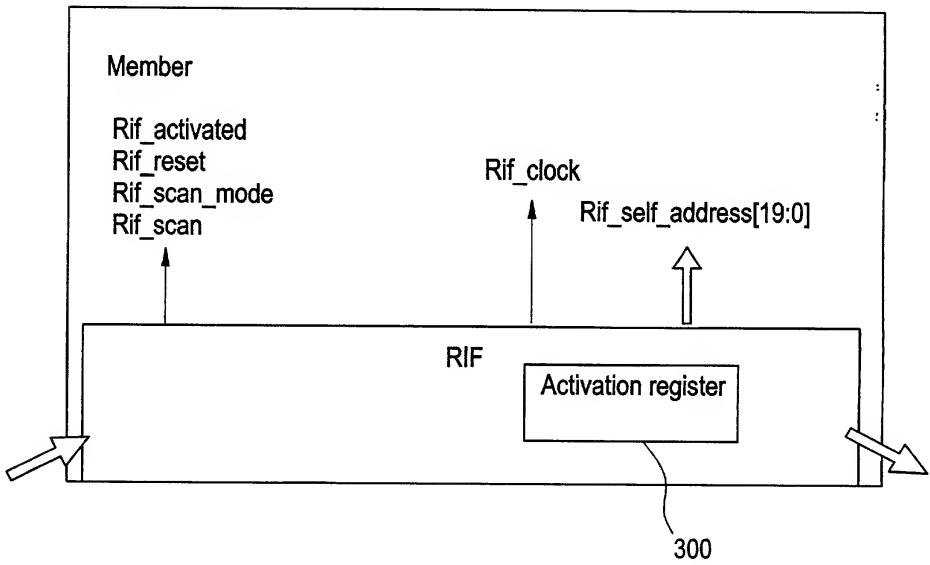


FIG. 34

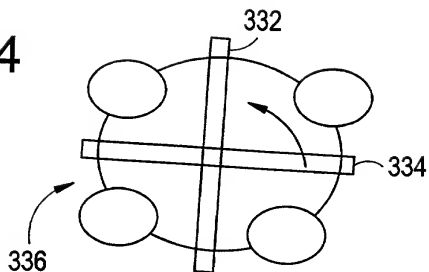


FIG. 35

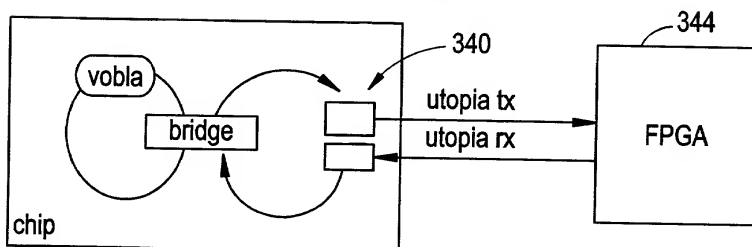


FIG. 36

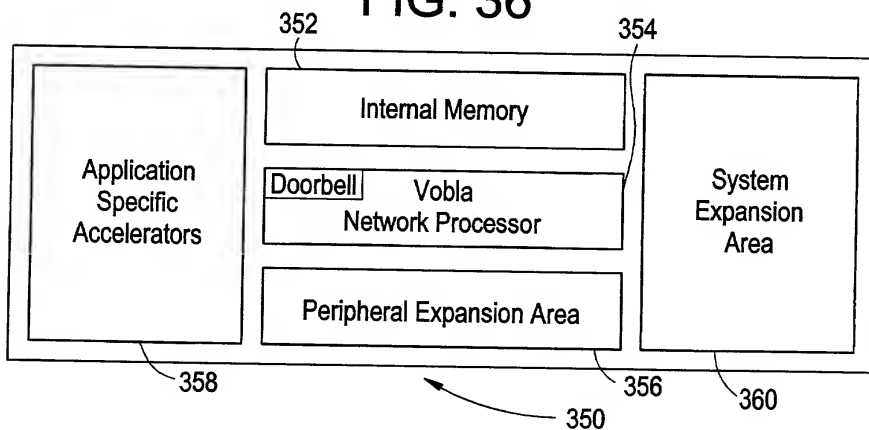
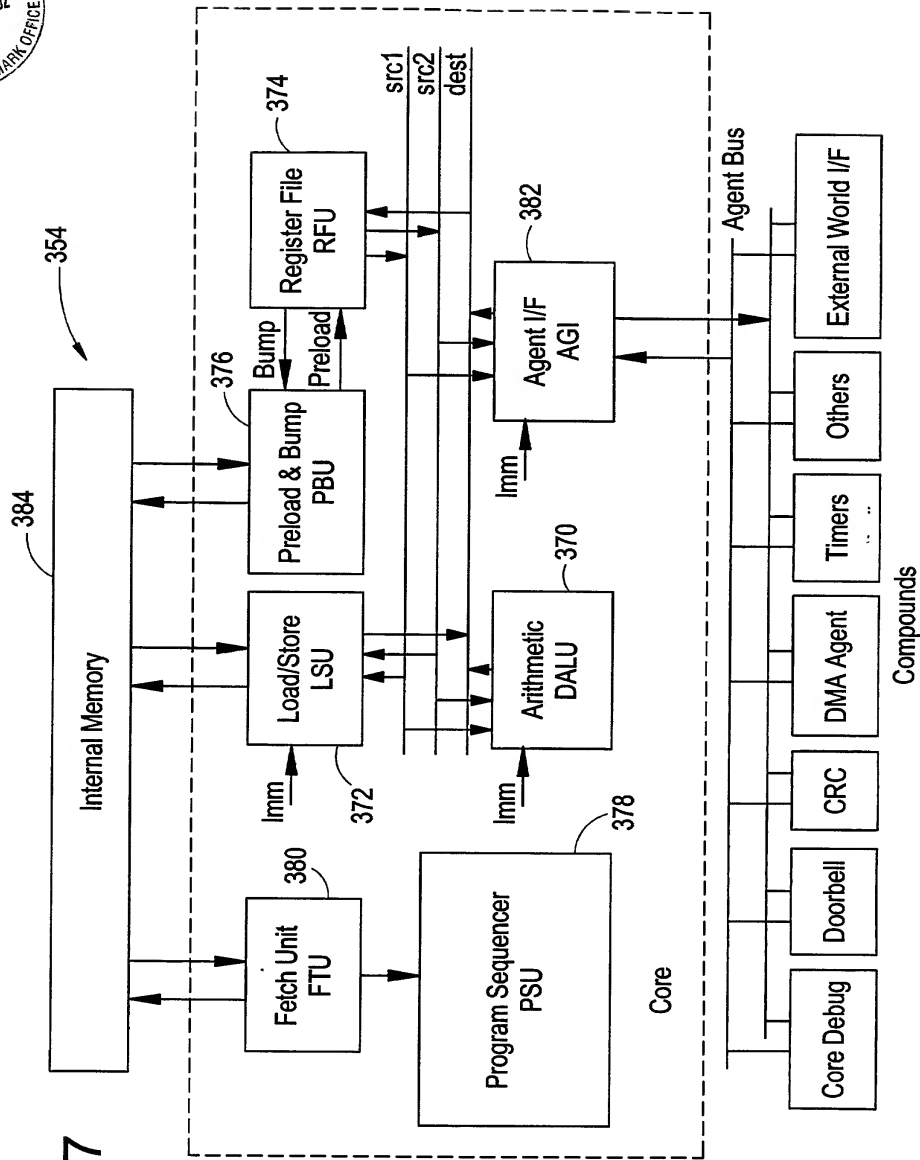
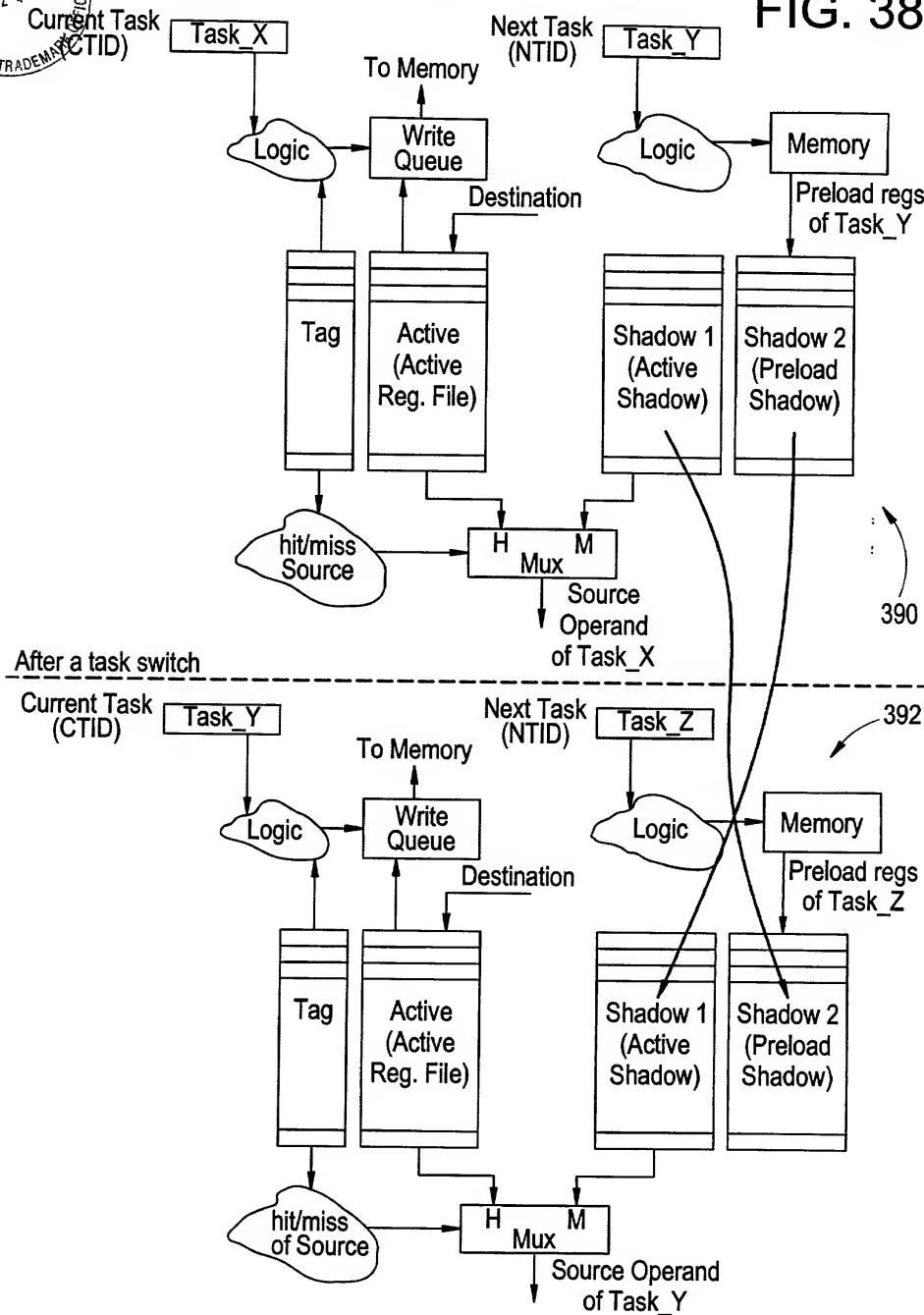


FIG. 37



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FIG. 38



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FIG. 39

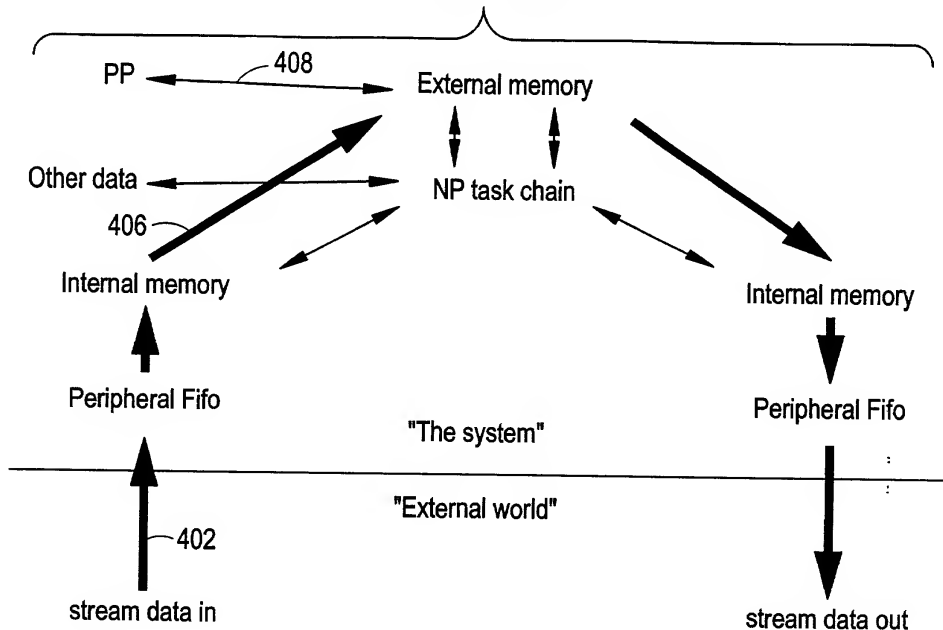


FIG. 40

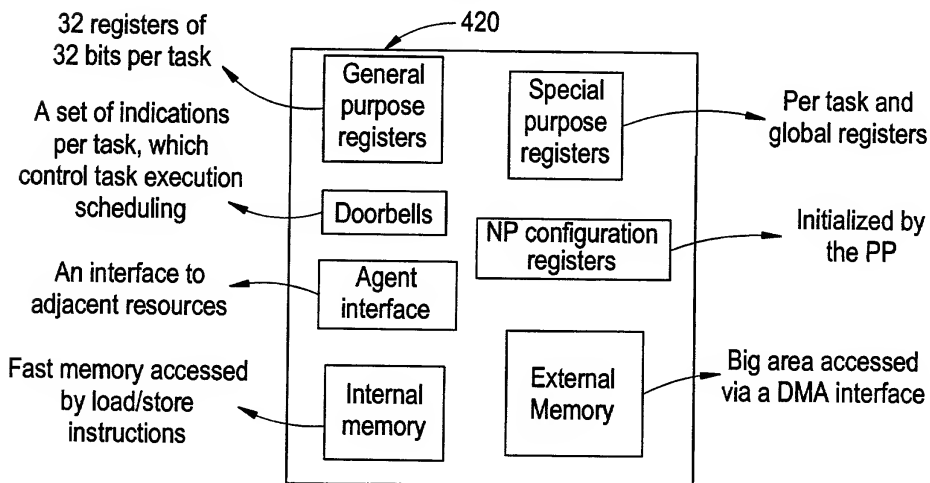


FIG. 41

R1 register

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0



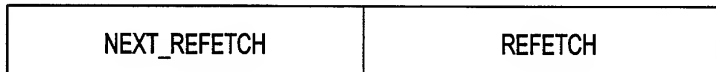
s - sticky bit
eq - equal/zero
lt - less then/negative
gt - greater then/positive
c - carry
mb - reflection of the RAM multi-reader busy indication

430

FIG. 42

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

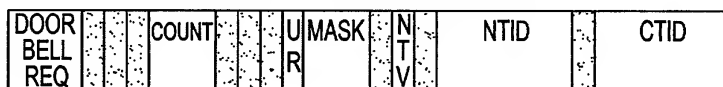
REFETCH SPR
(spr index - 0)



440

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

TASK SPR
(spr index - 1)



442

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

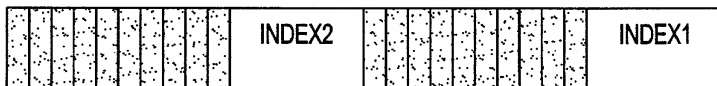
TRAP SPR
(spr index - 2)



444

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

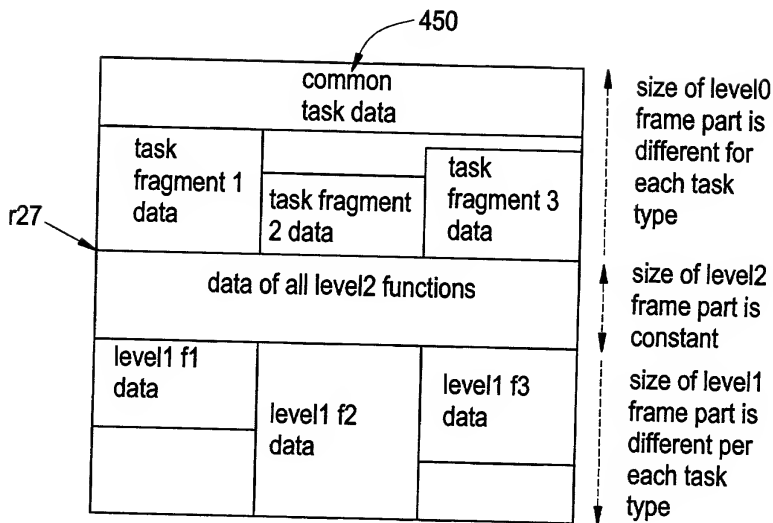
MINDEX SPR
(spr index - 3)



446

20140504 10:24:02

FIG. 43



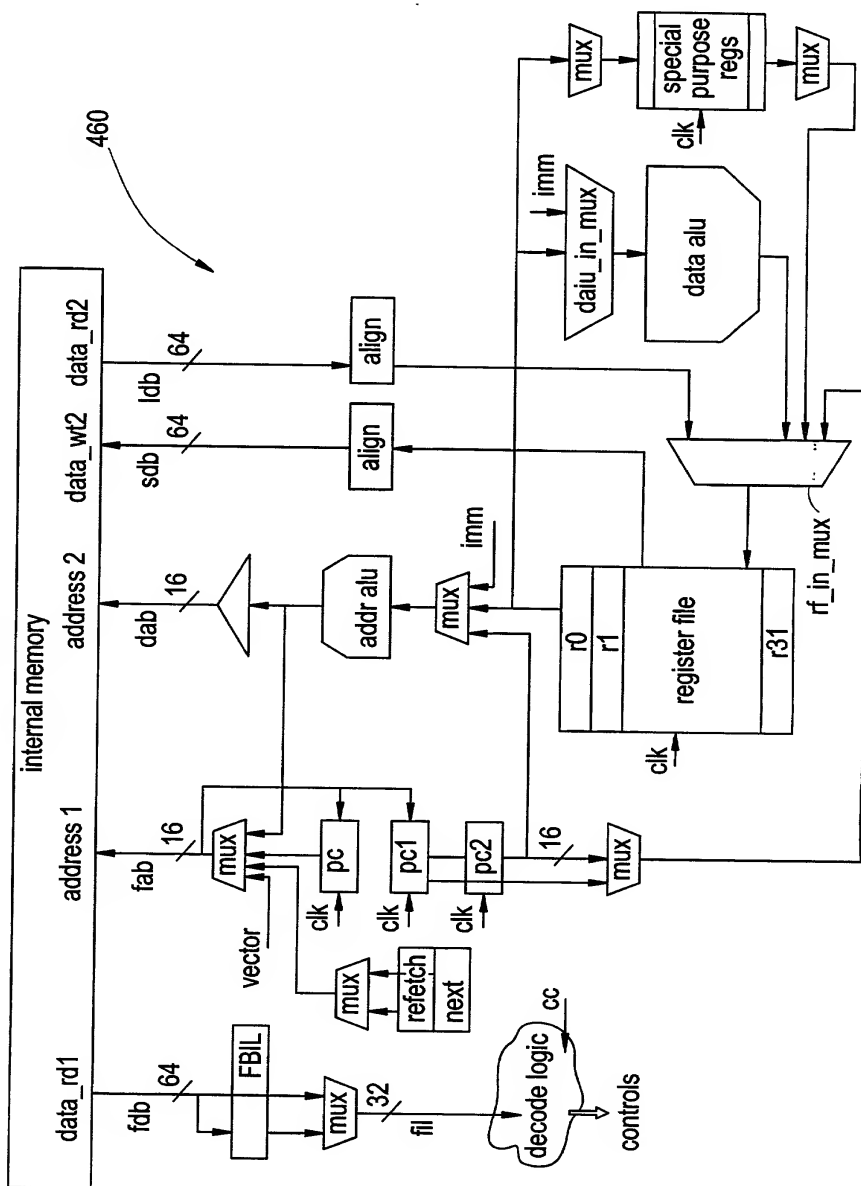
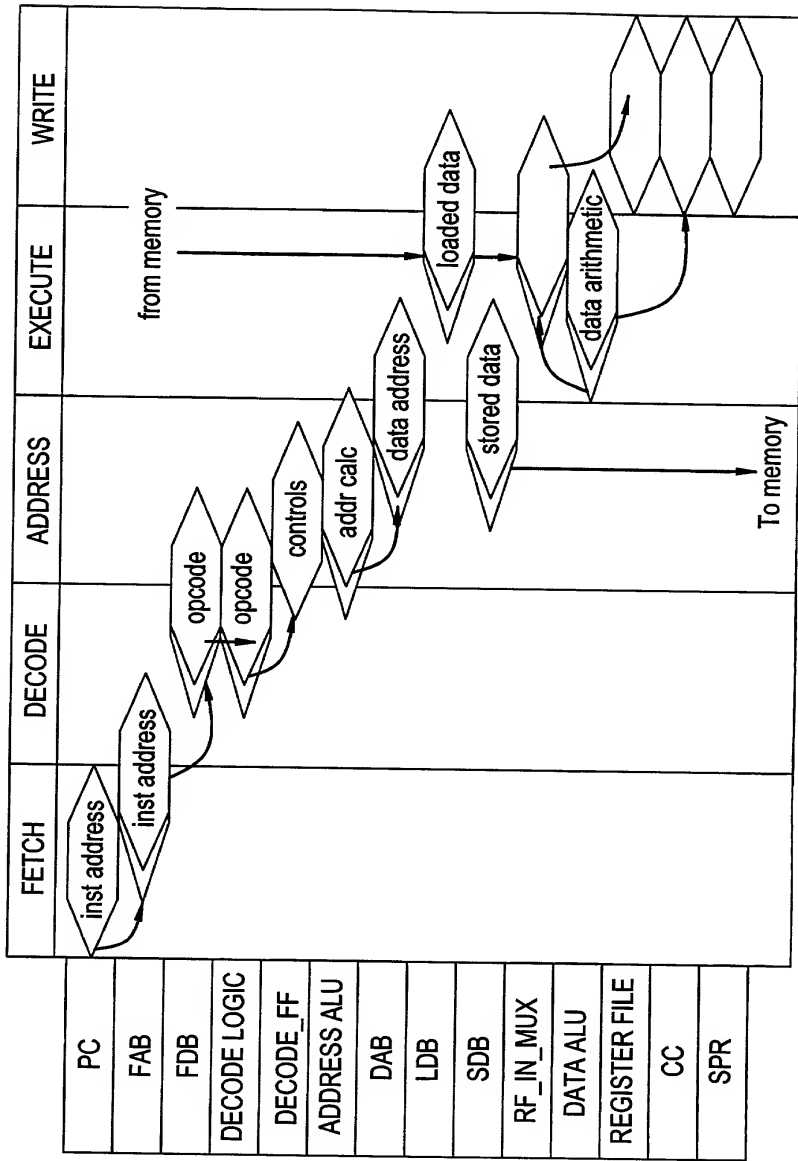


FIG. 45



480

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Logic

Flip Flop

FIG. 46

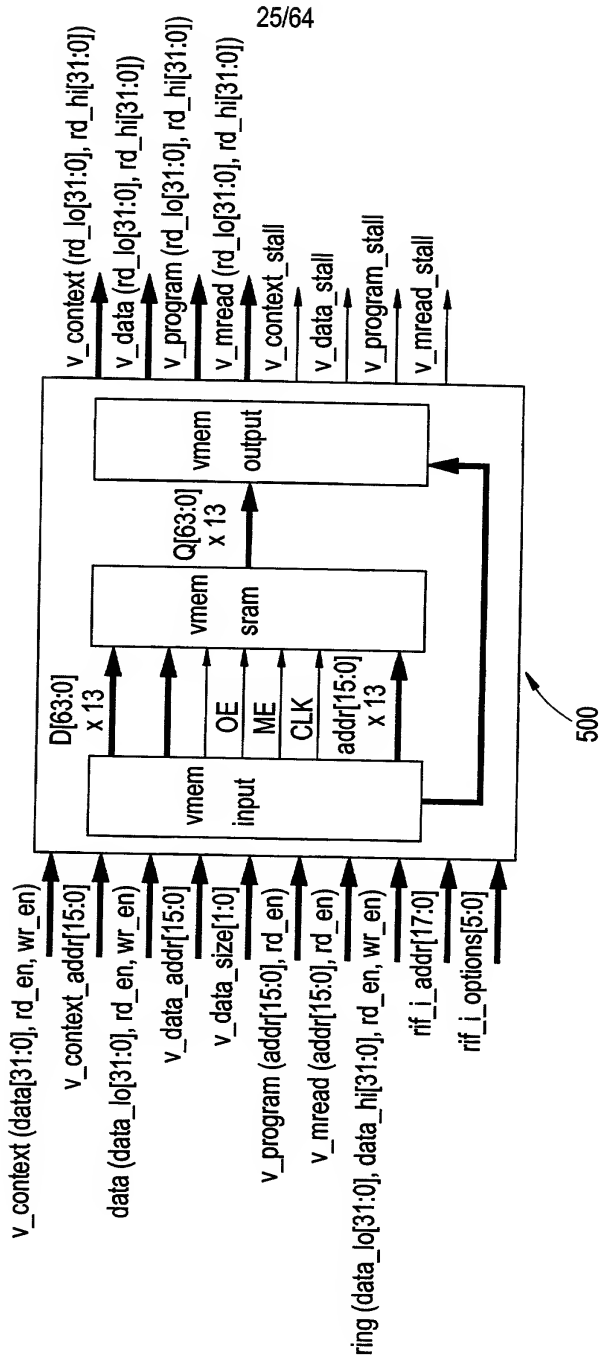


FIG. 47

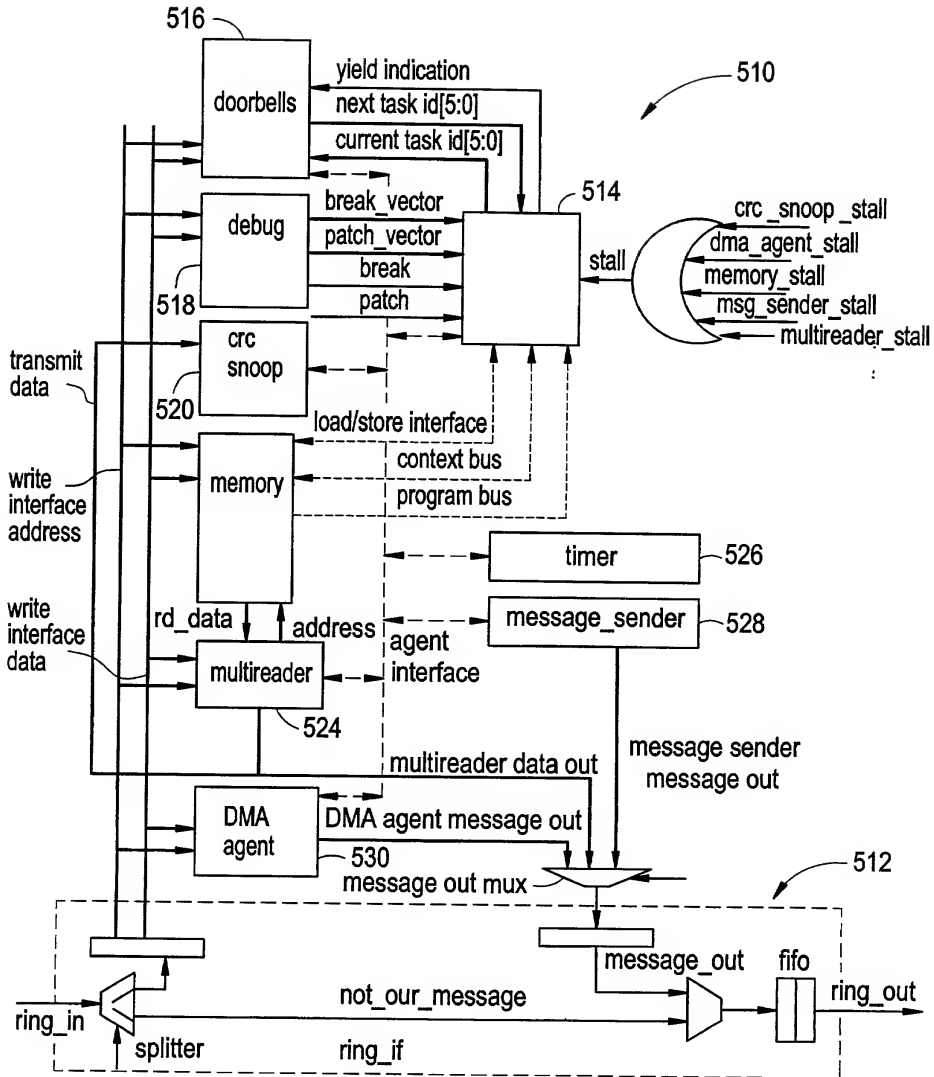
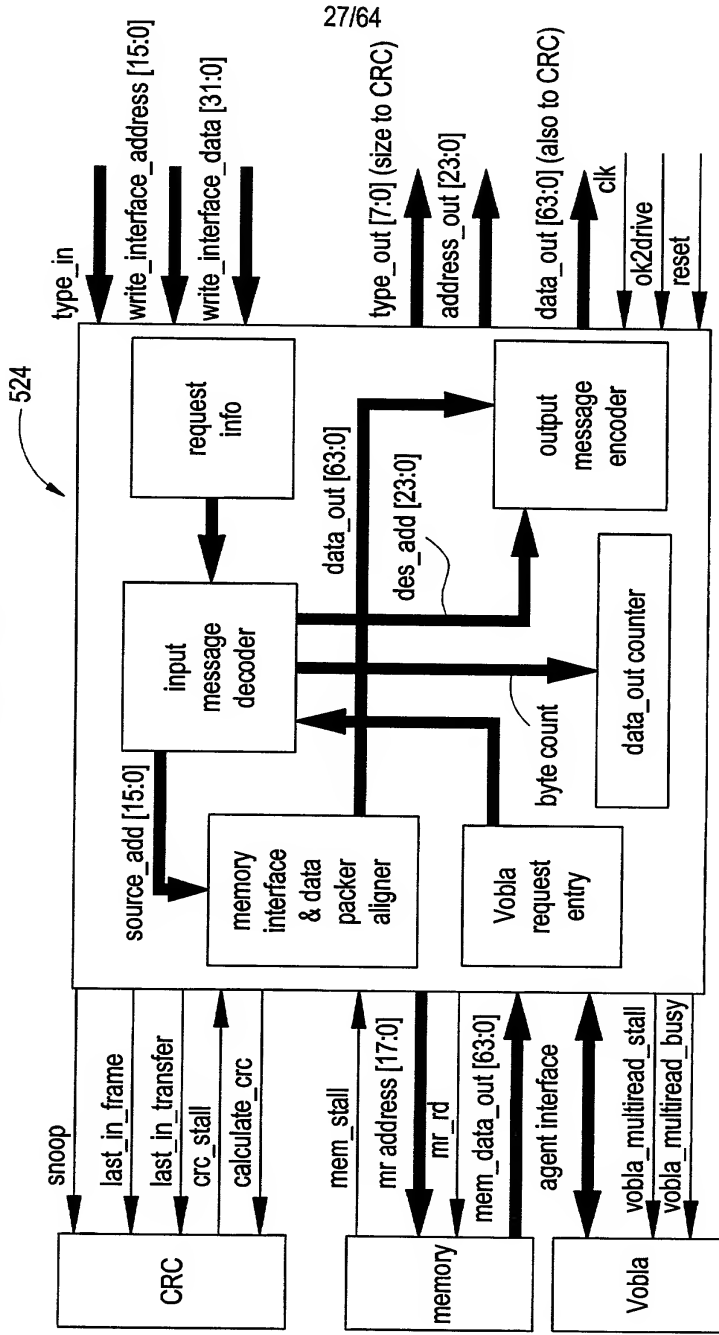


FIG. 48



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FIG. 49

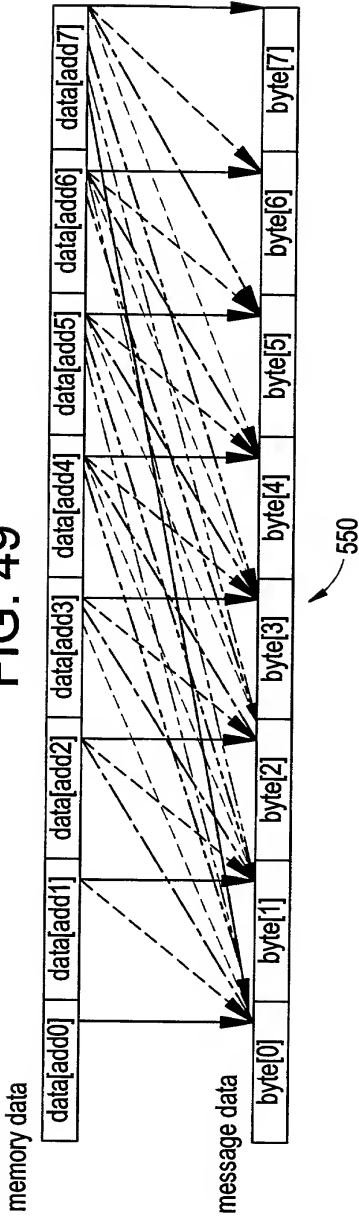
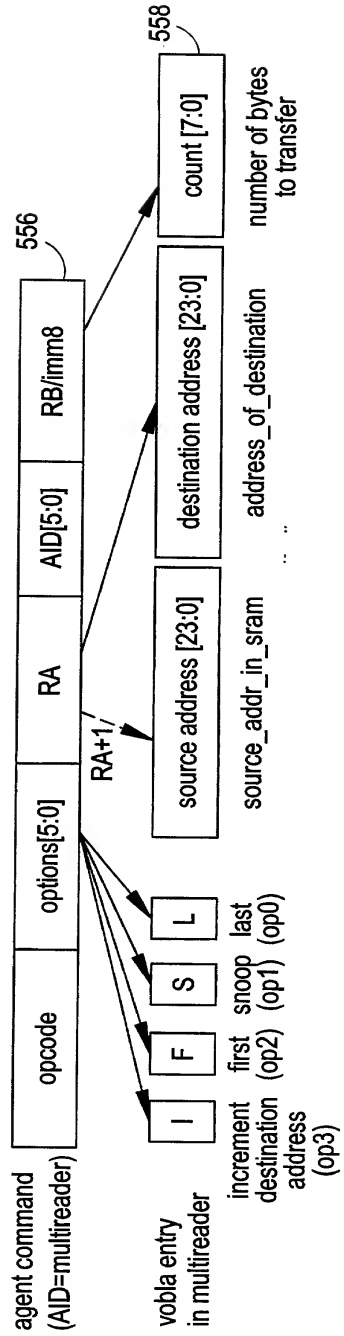


FIG. 50



204260" 84E4000T

FIG. 51

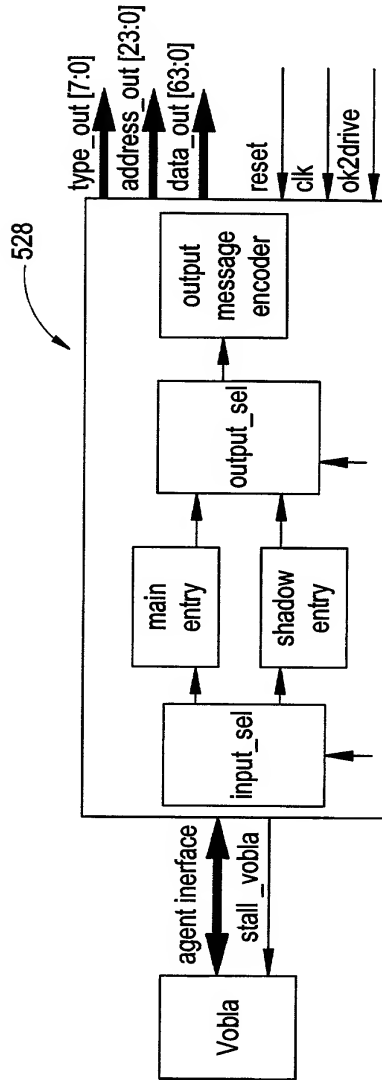


FIG. 52

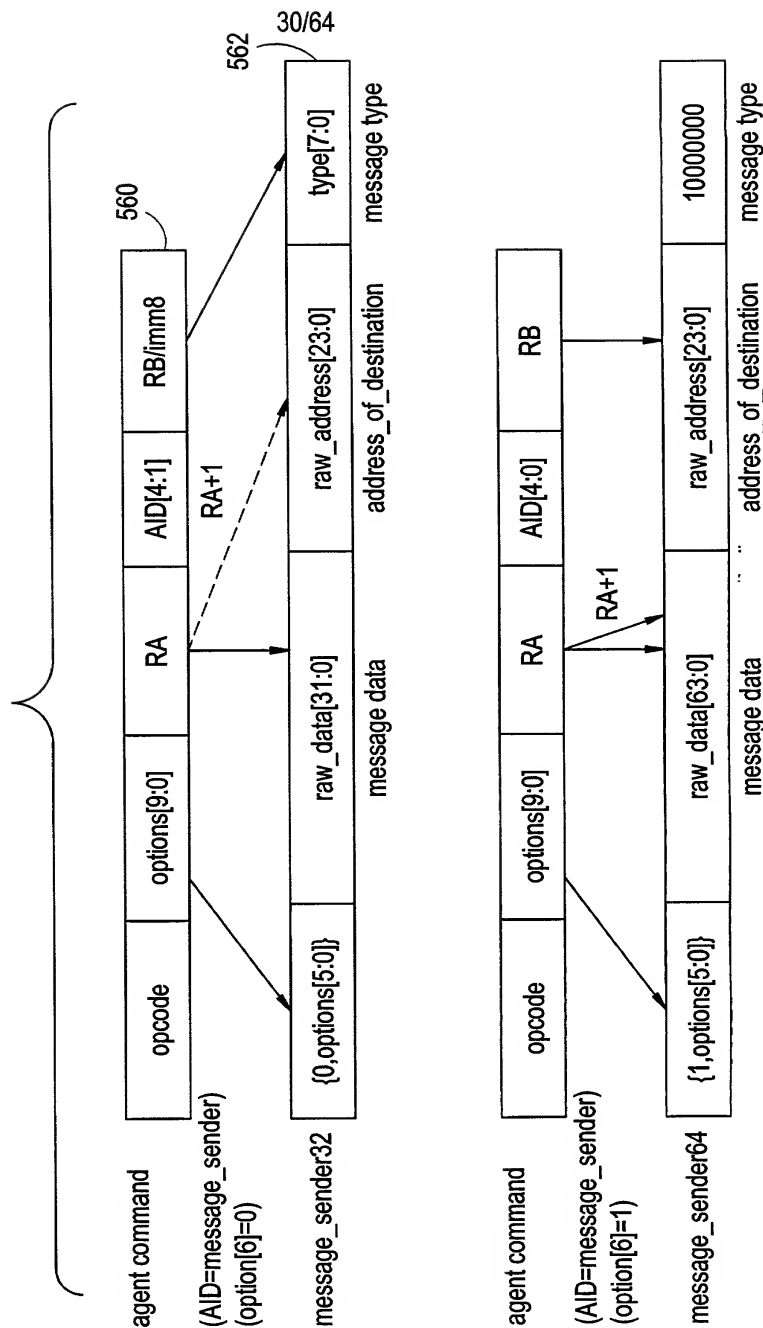


FIG. 53

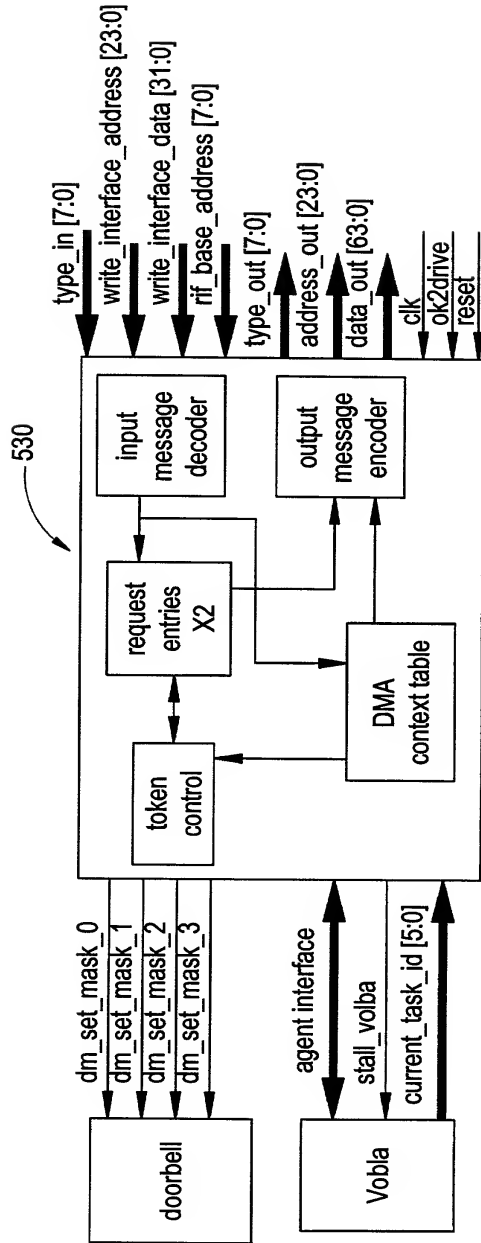




FIG. 54

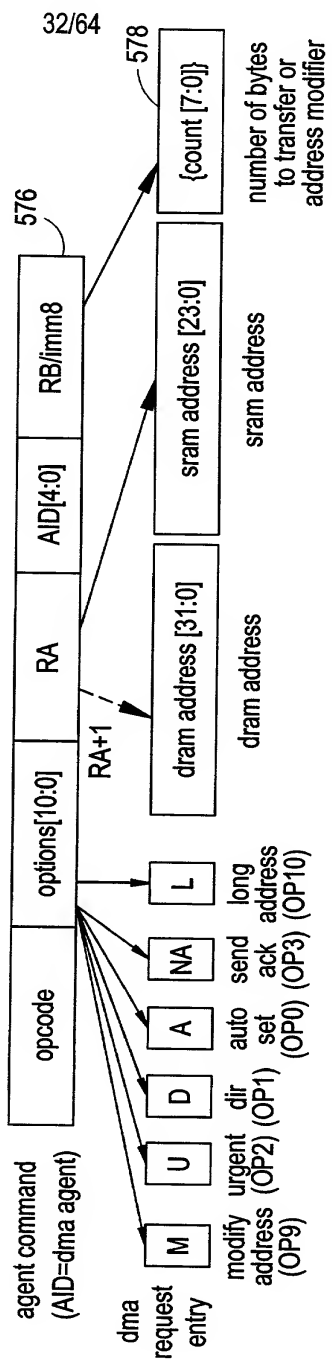
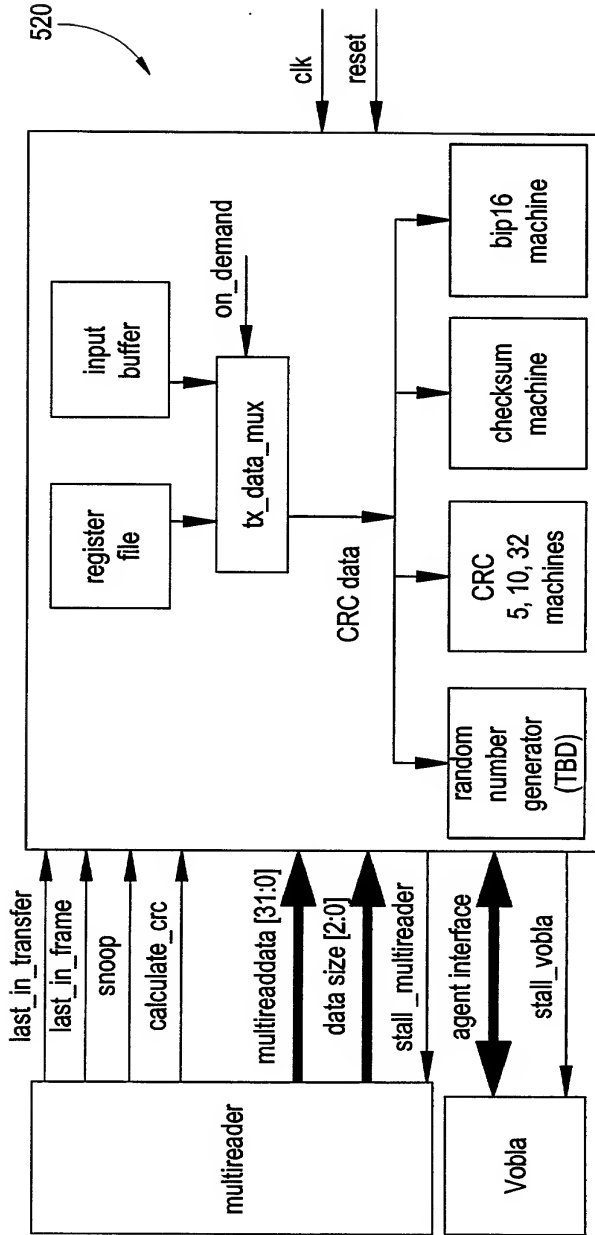


FIG. 55



204260" E4E4900T

FIG. 56

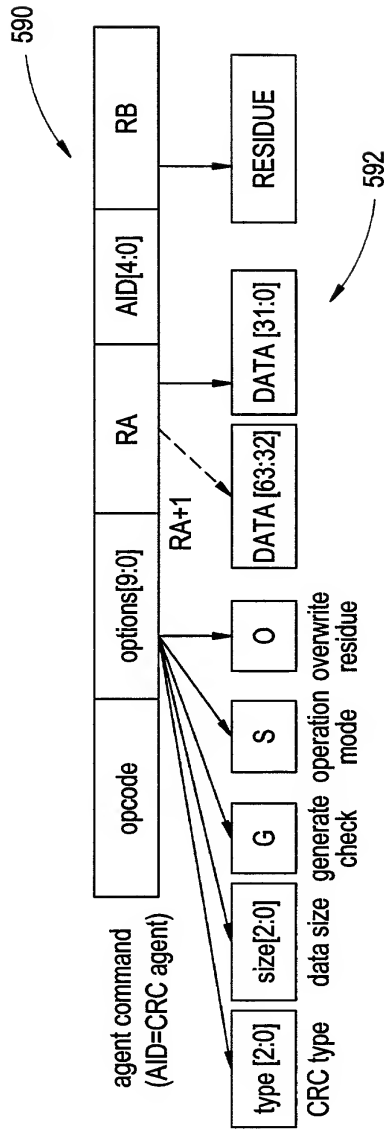


FIG. 57

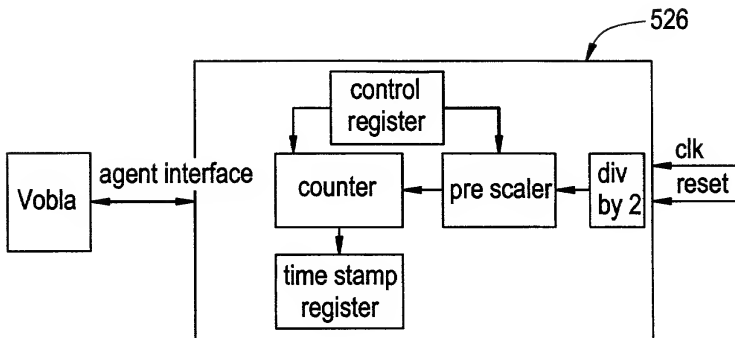


FIG. 58

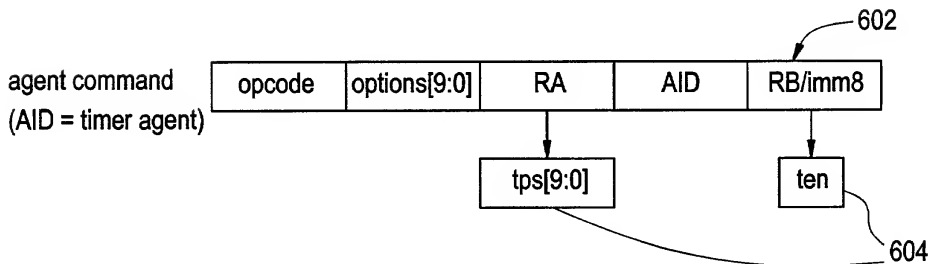


FIG. 61

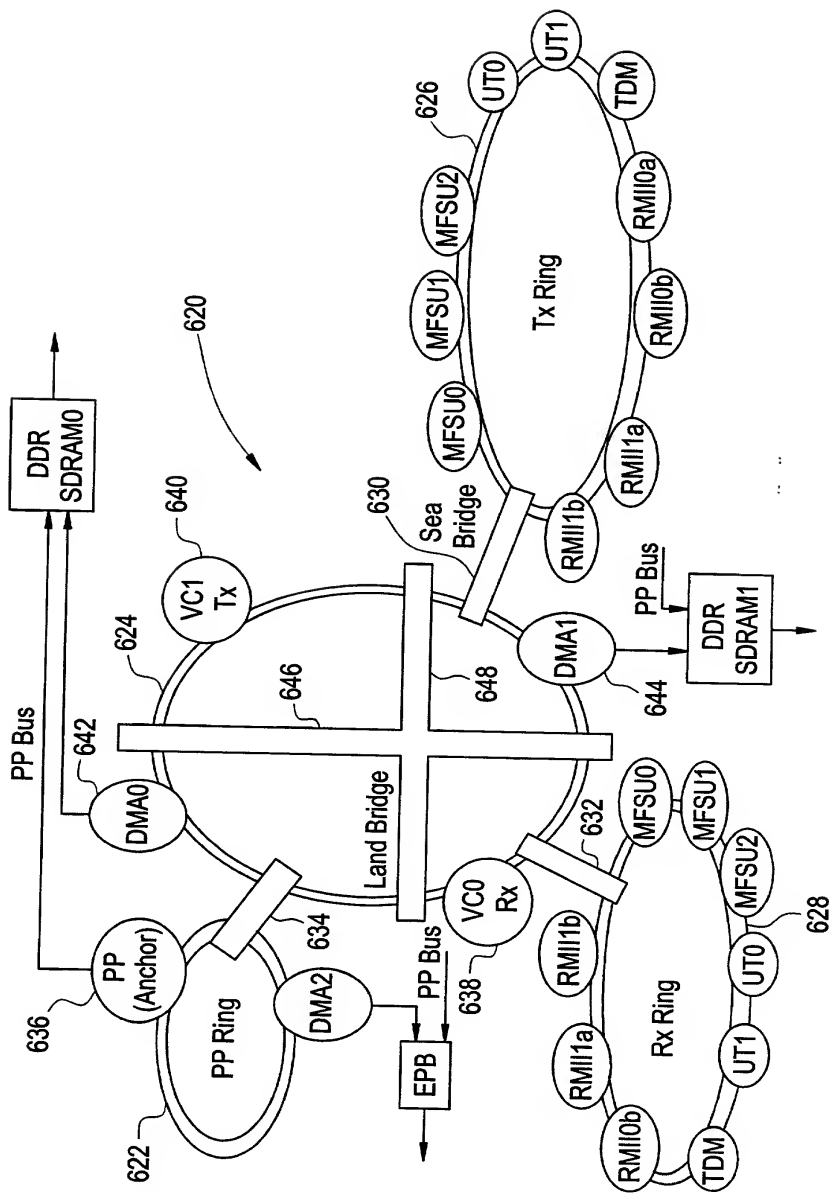


FIG. 62

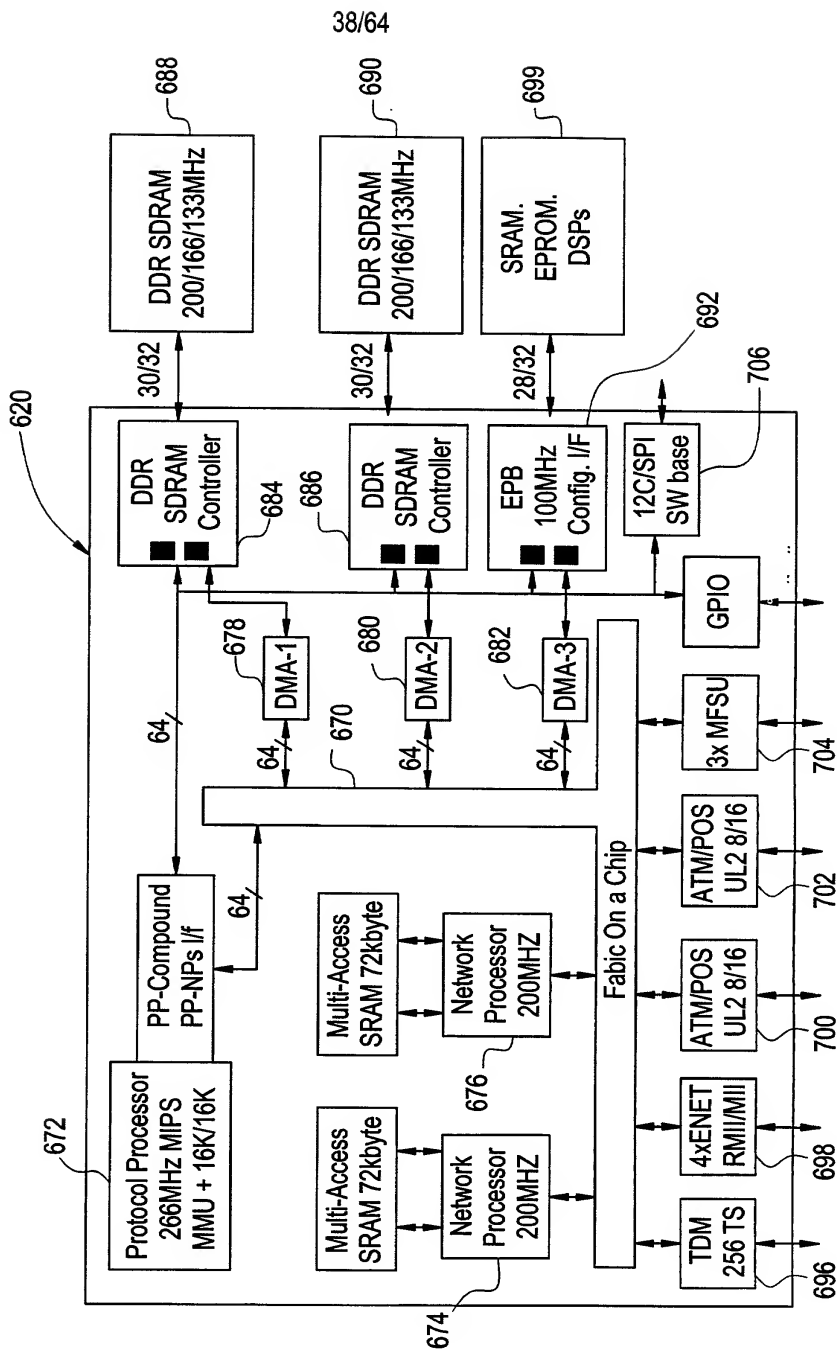


FIG. 63

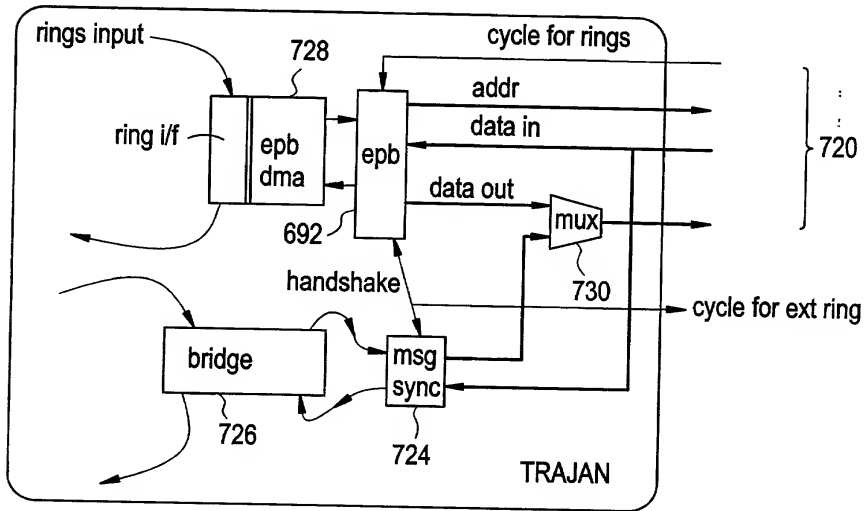


FIG. 64

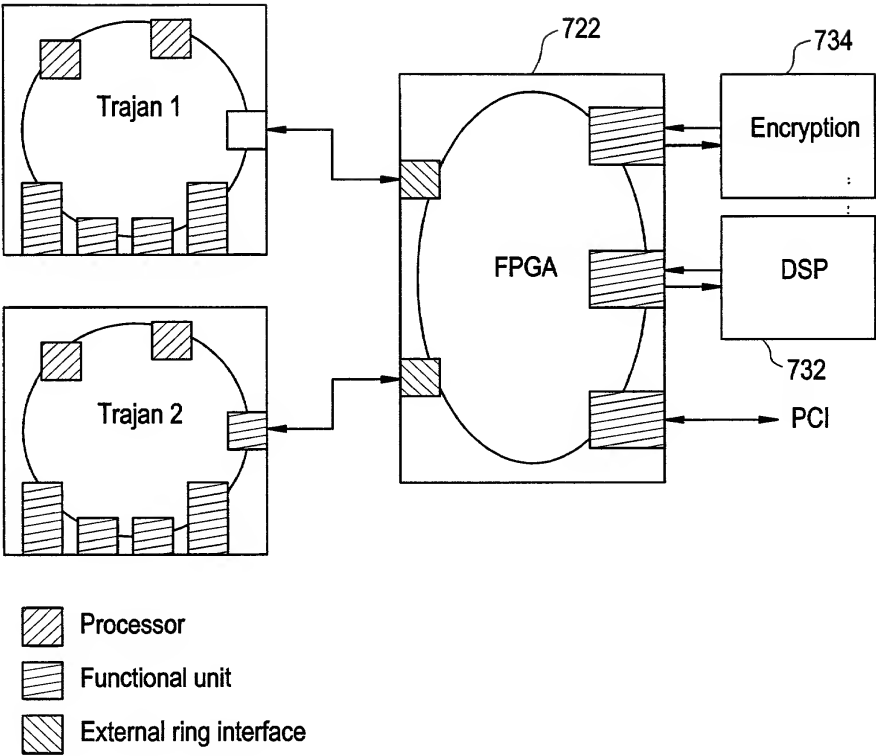


FIG. 65

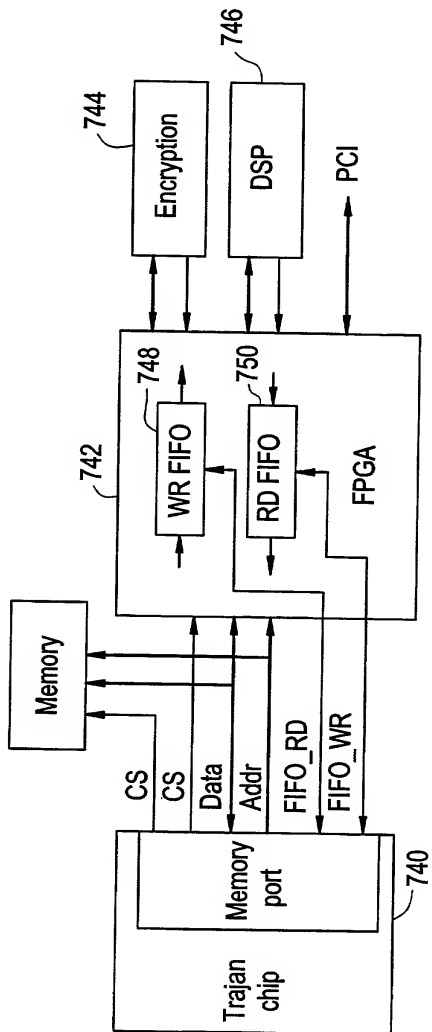


FIG. 66

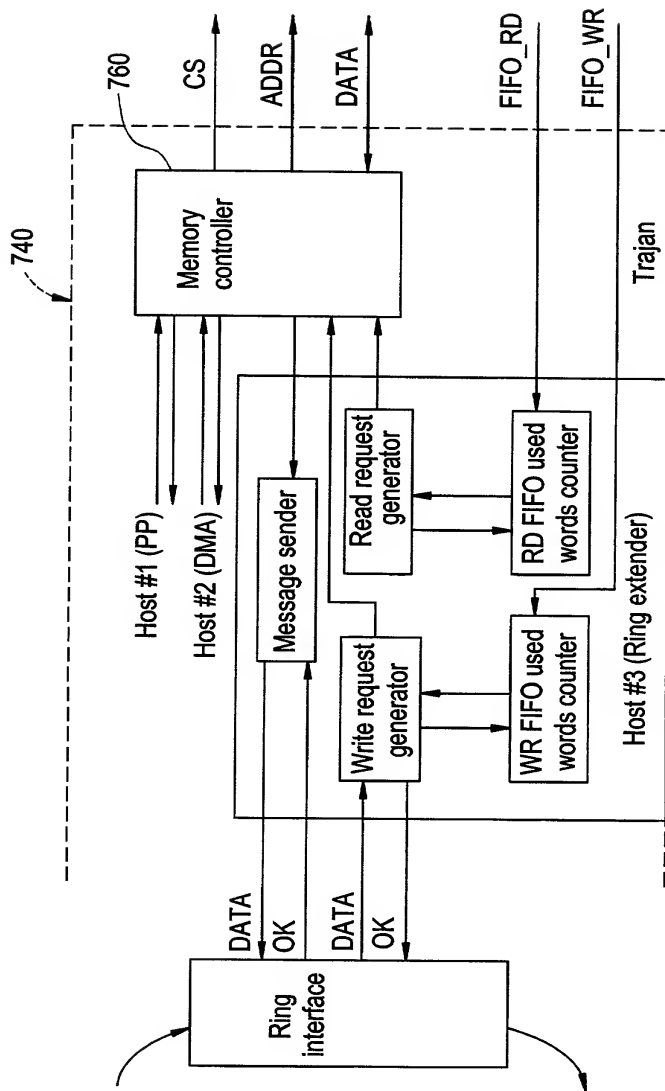


FIG. 67

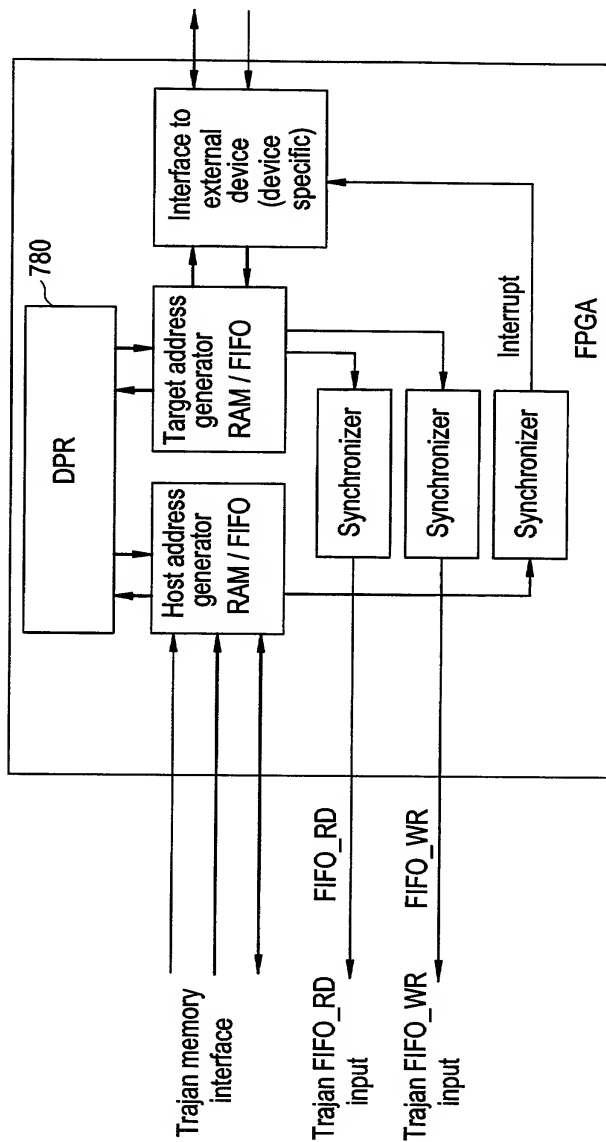


FIG. 68

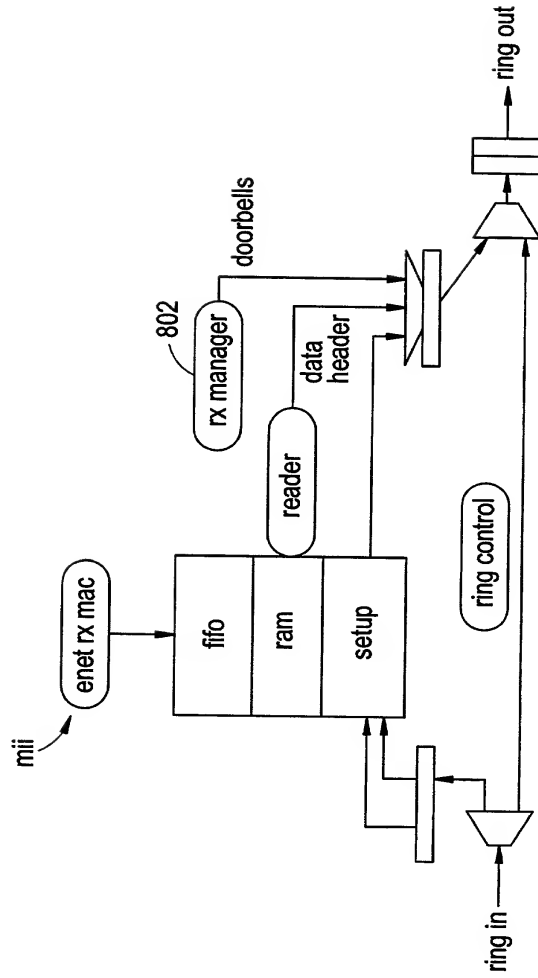
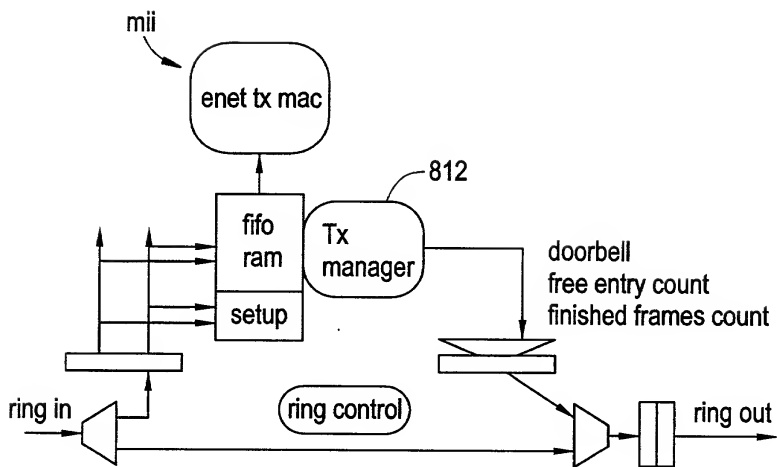
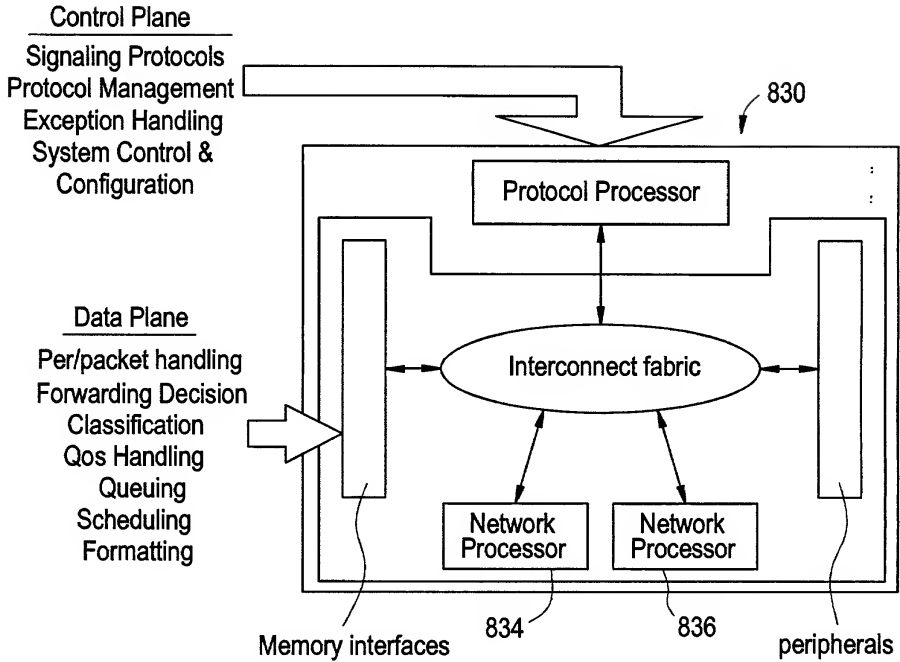


FIG. 69



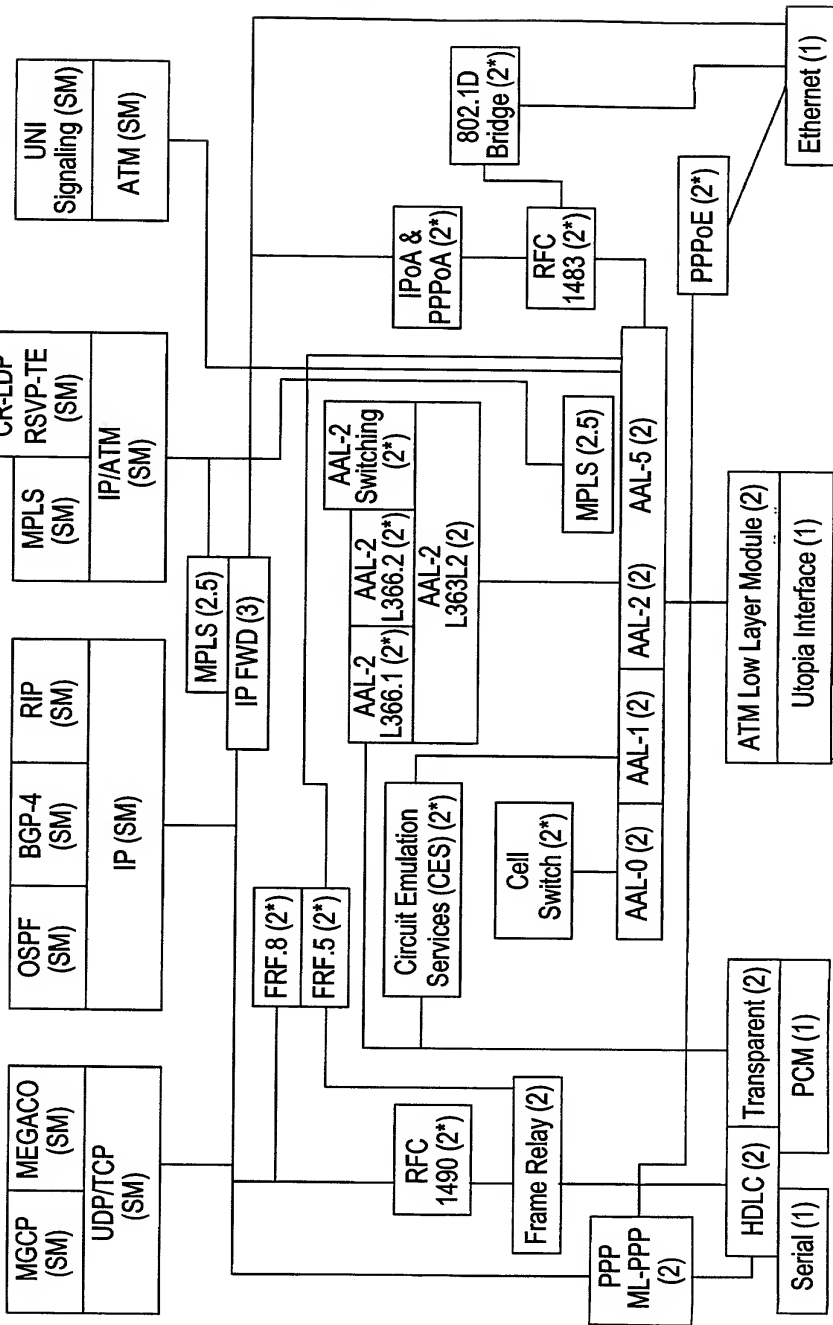
204260-54459001

FIG. 70



2042250.1-444001

FIG. 71



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204260" STE49001

FIG. 72

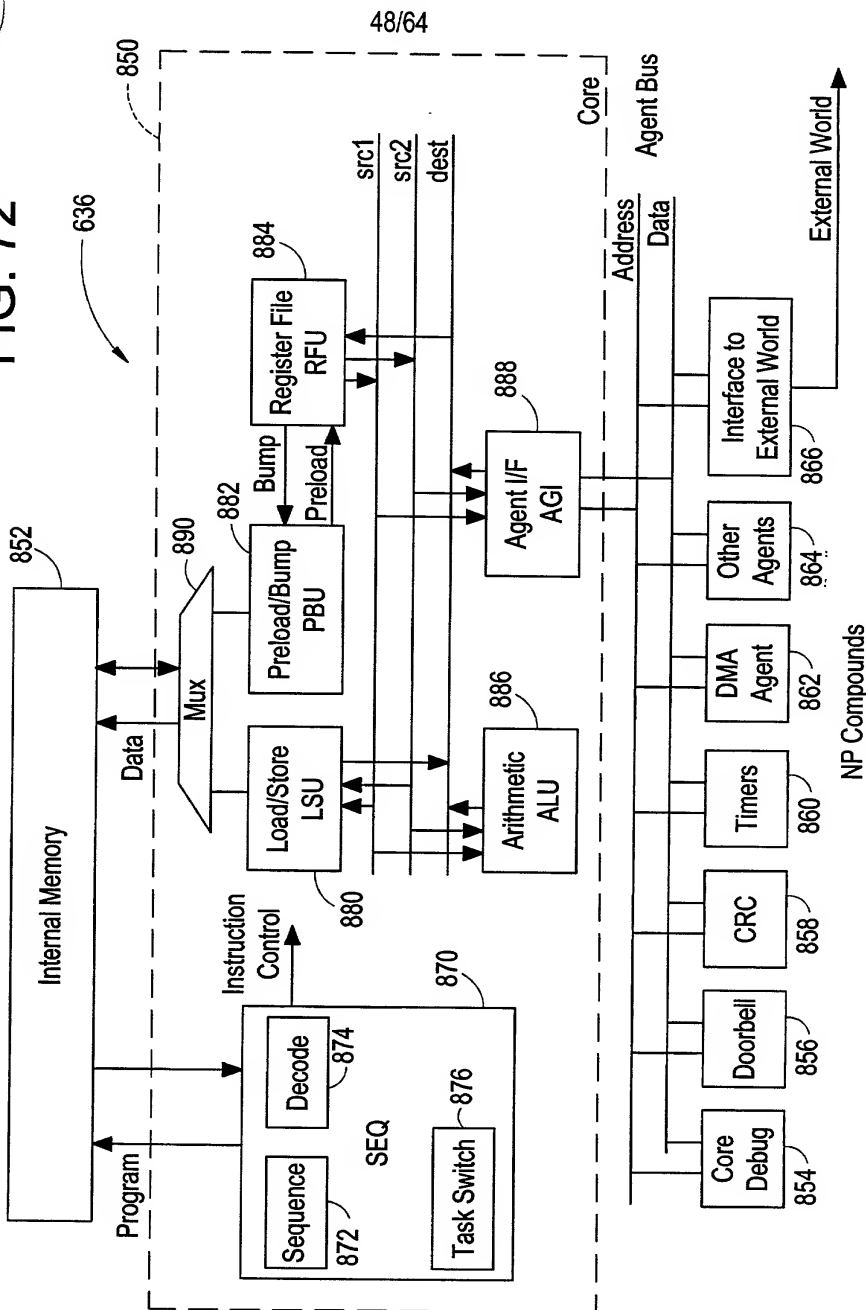




FIG. 73

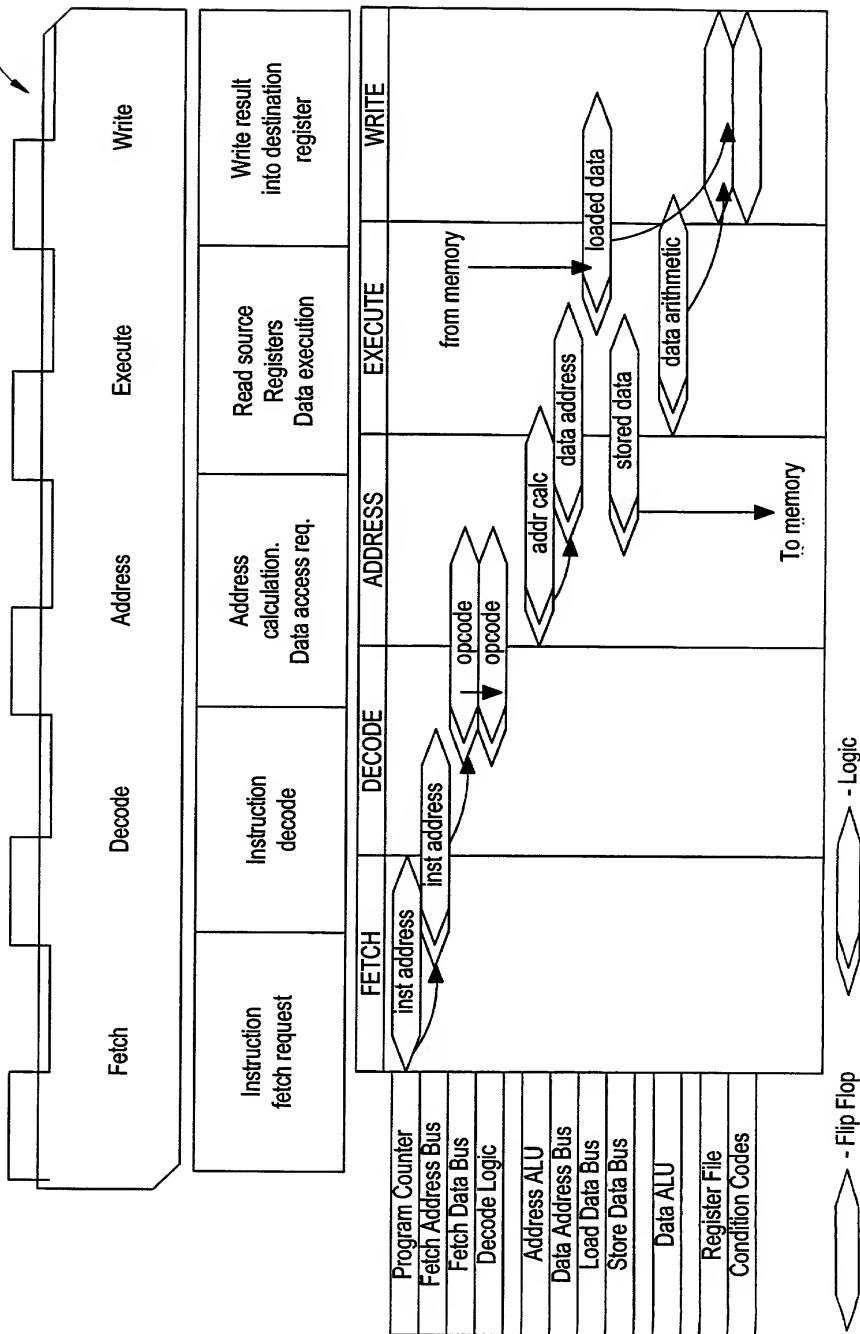


FIG. 74

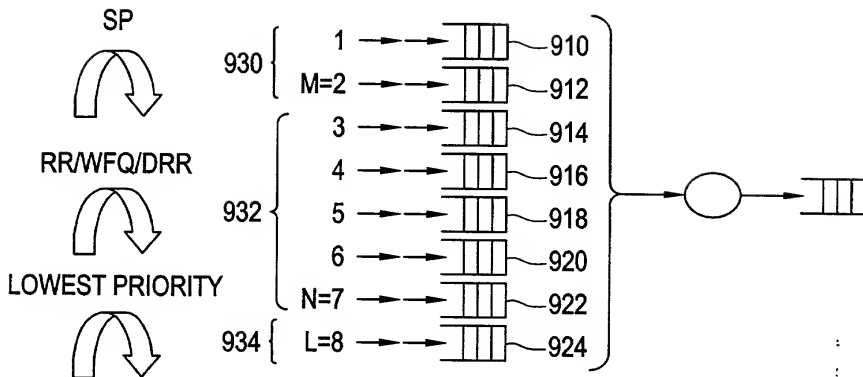


FIG. 75

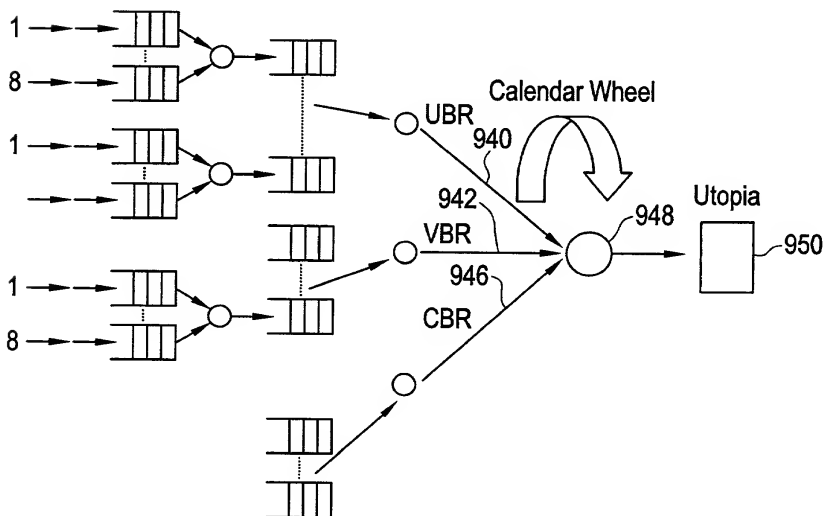


FIG. 76

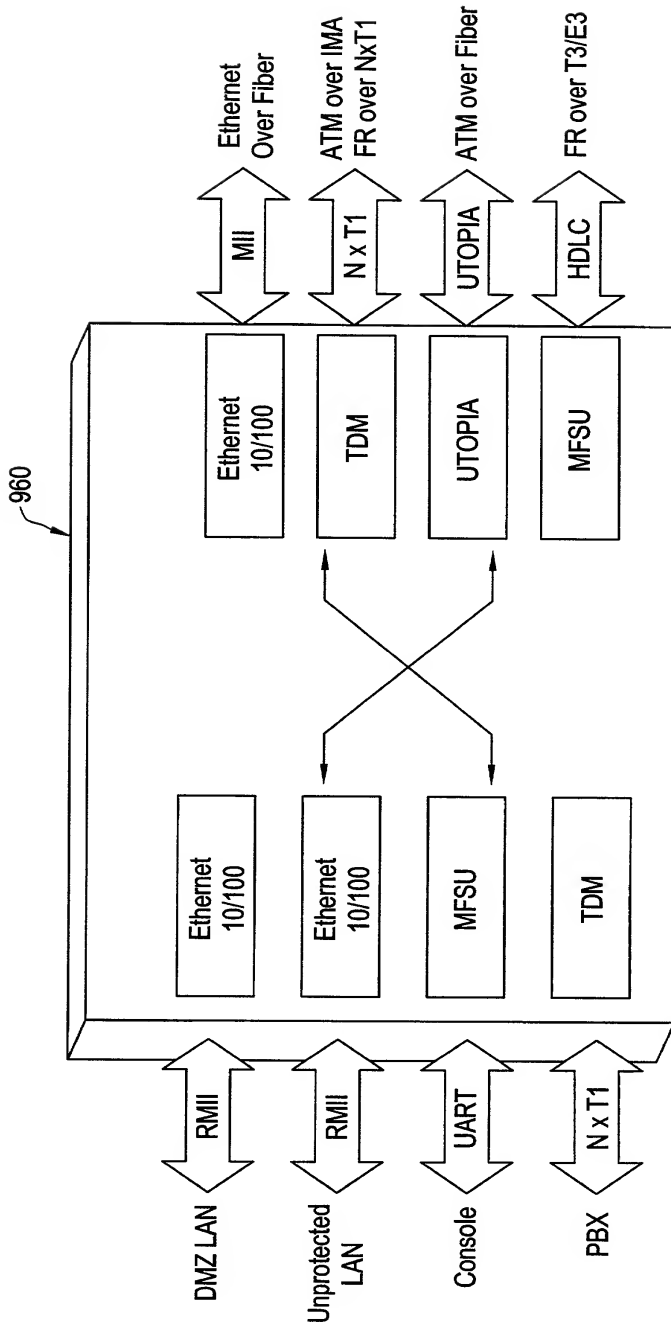


FIG. 77

970

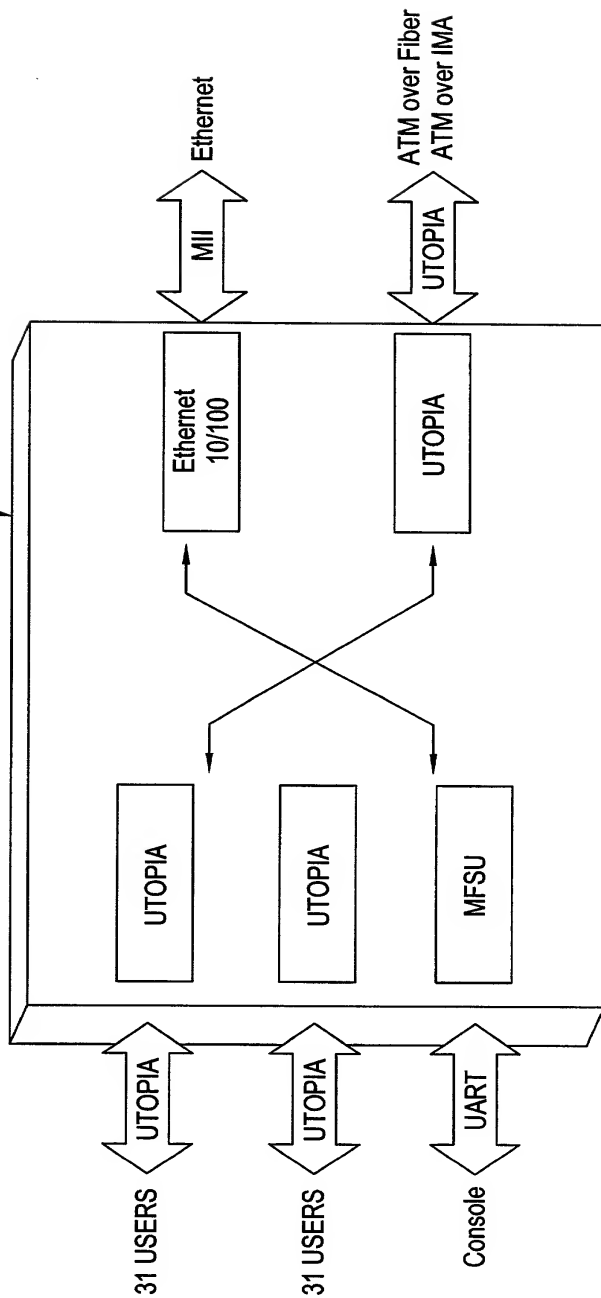


FIG. 78

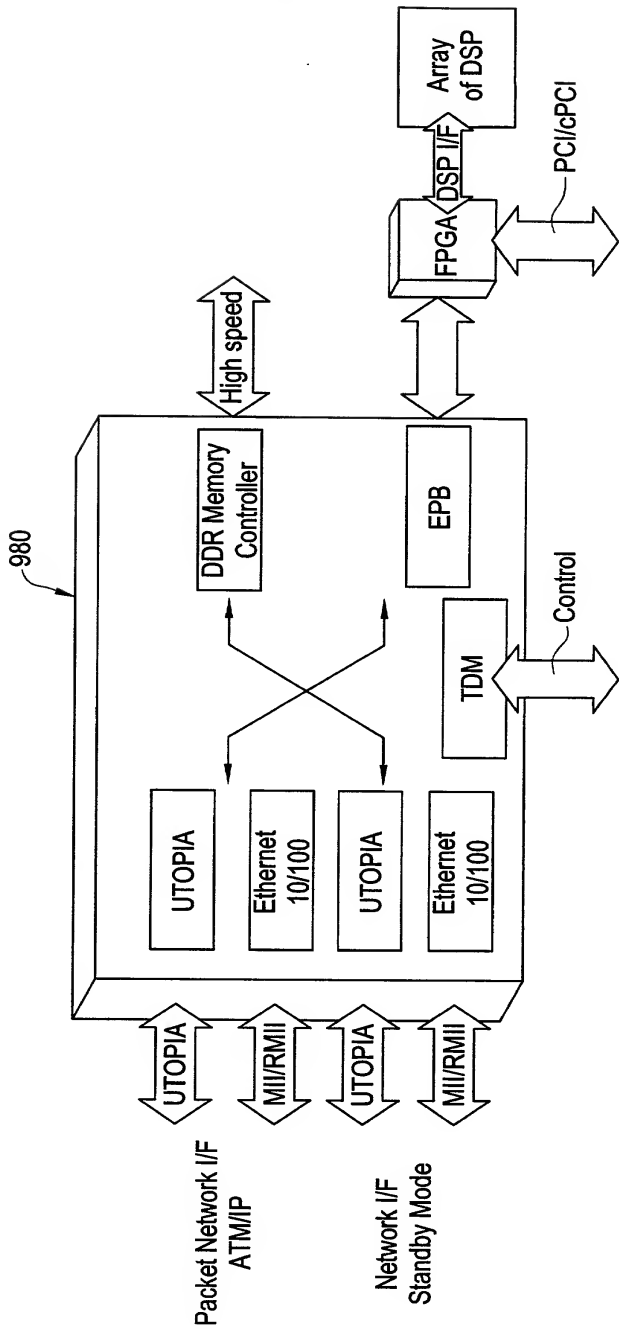


FIG. 79

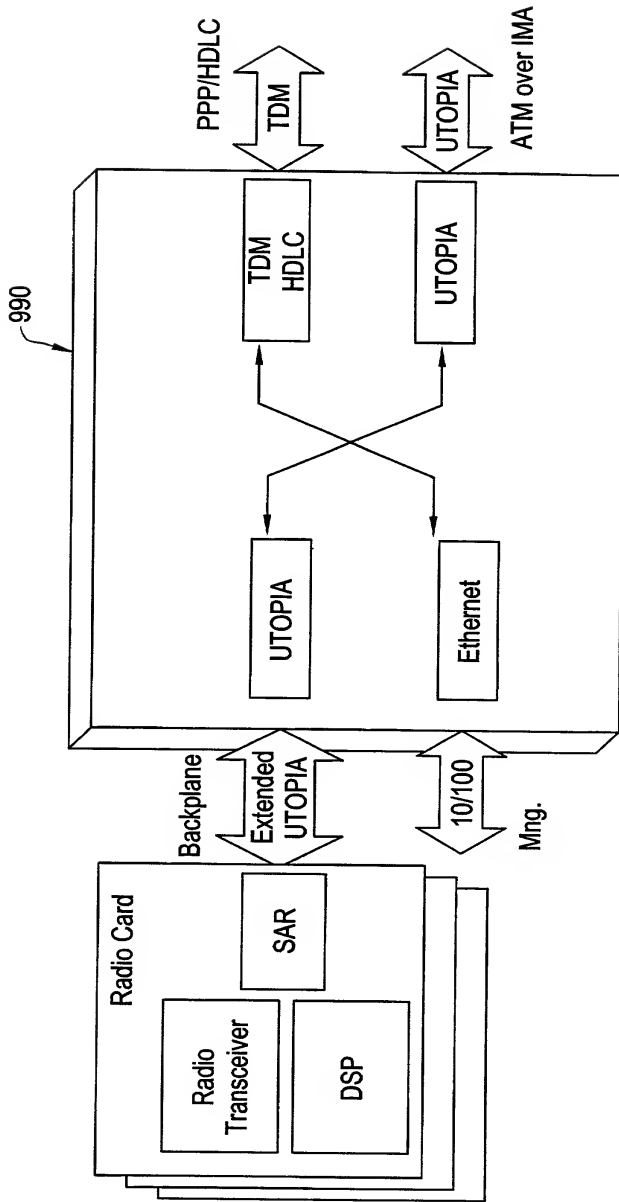
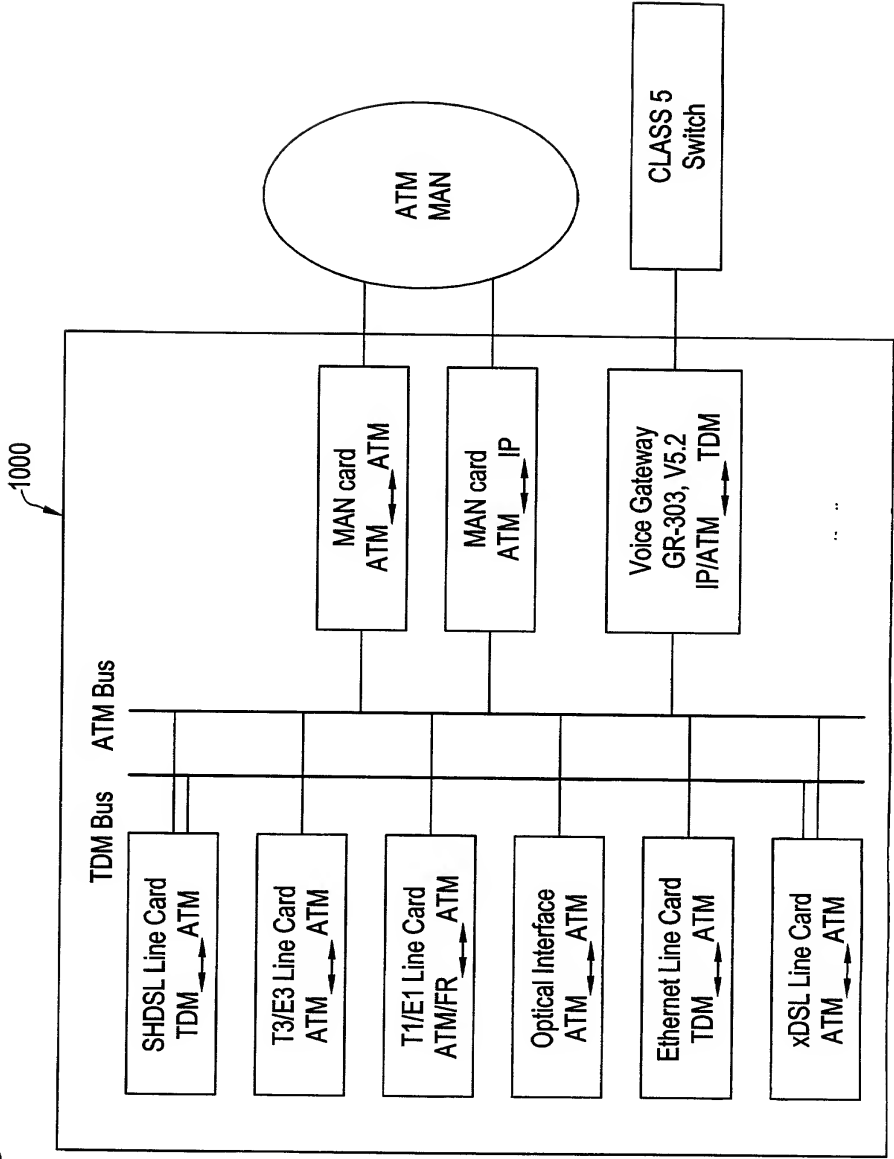


FIG. 80



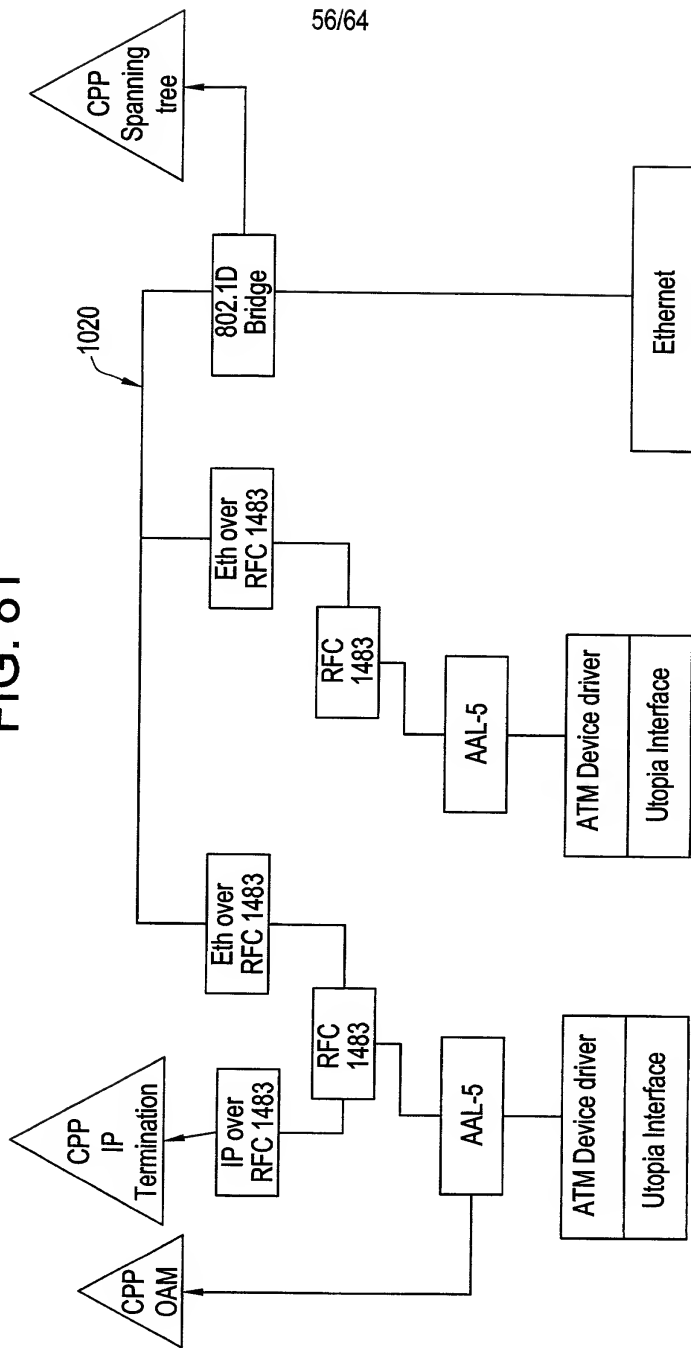


FIG. 82

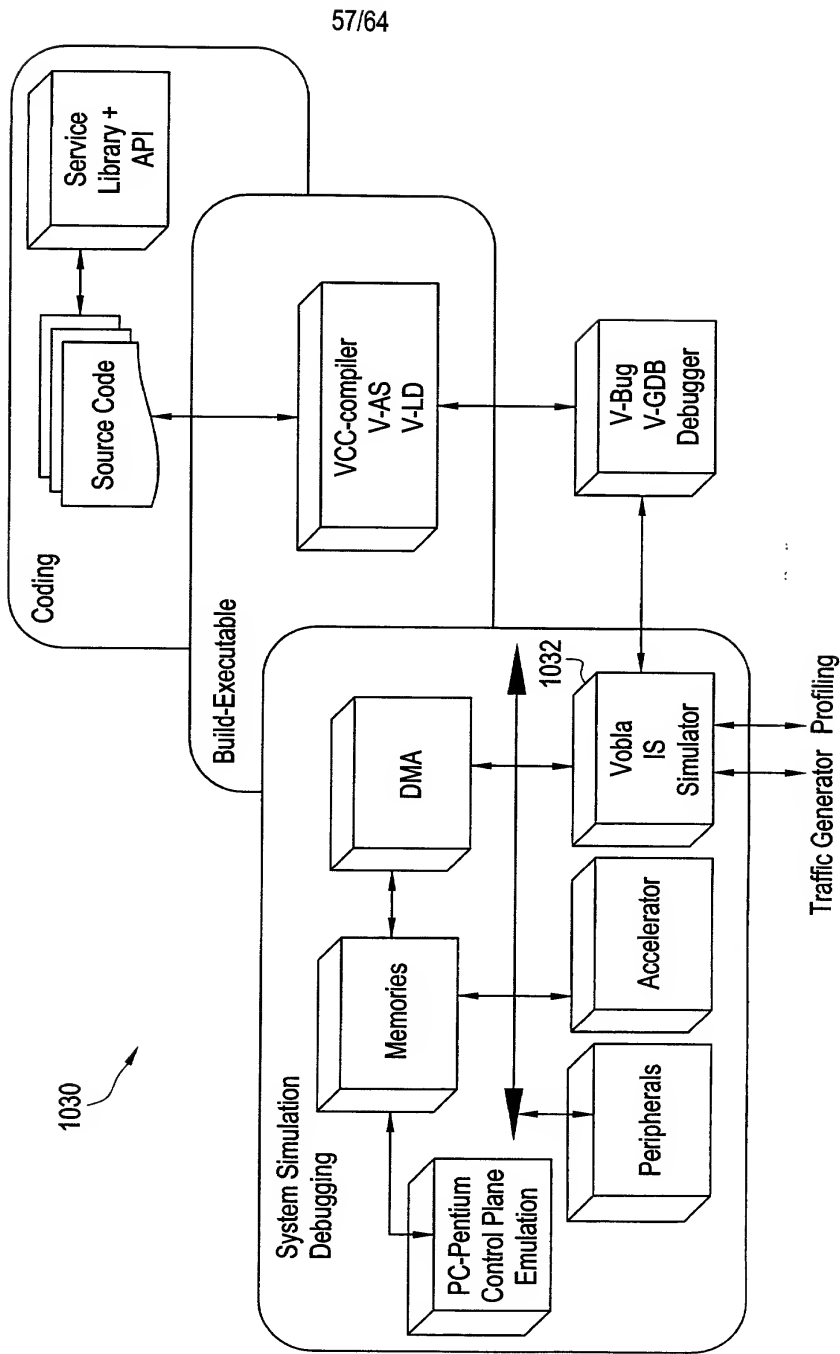


FIG. 83

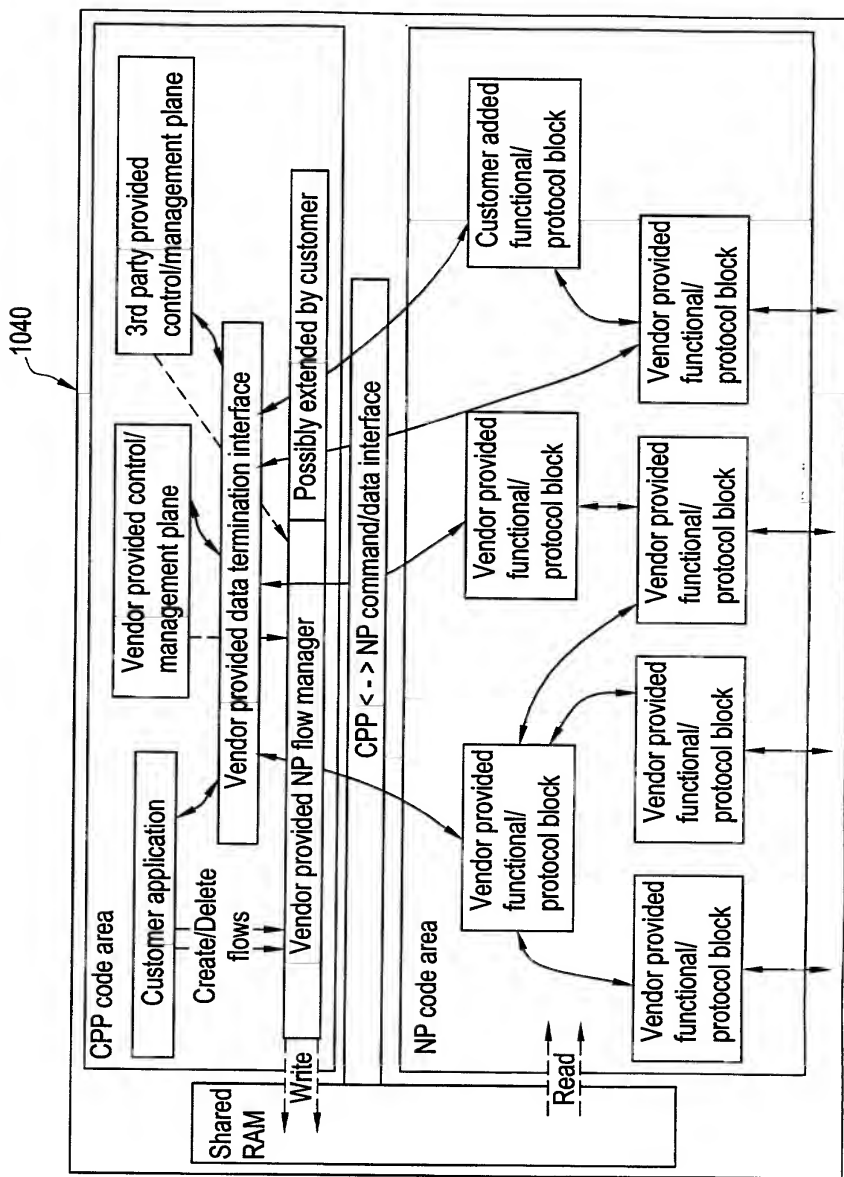
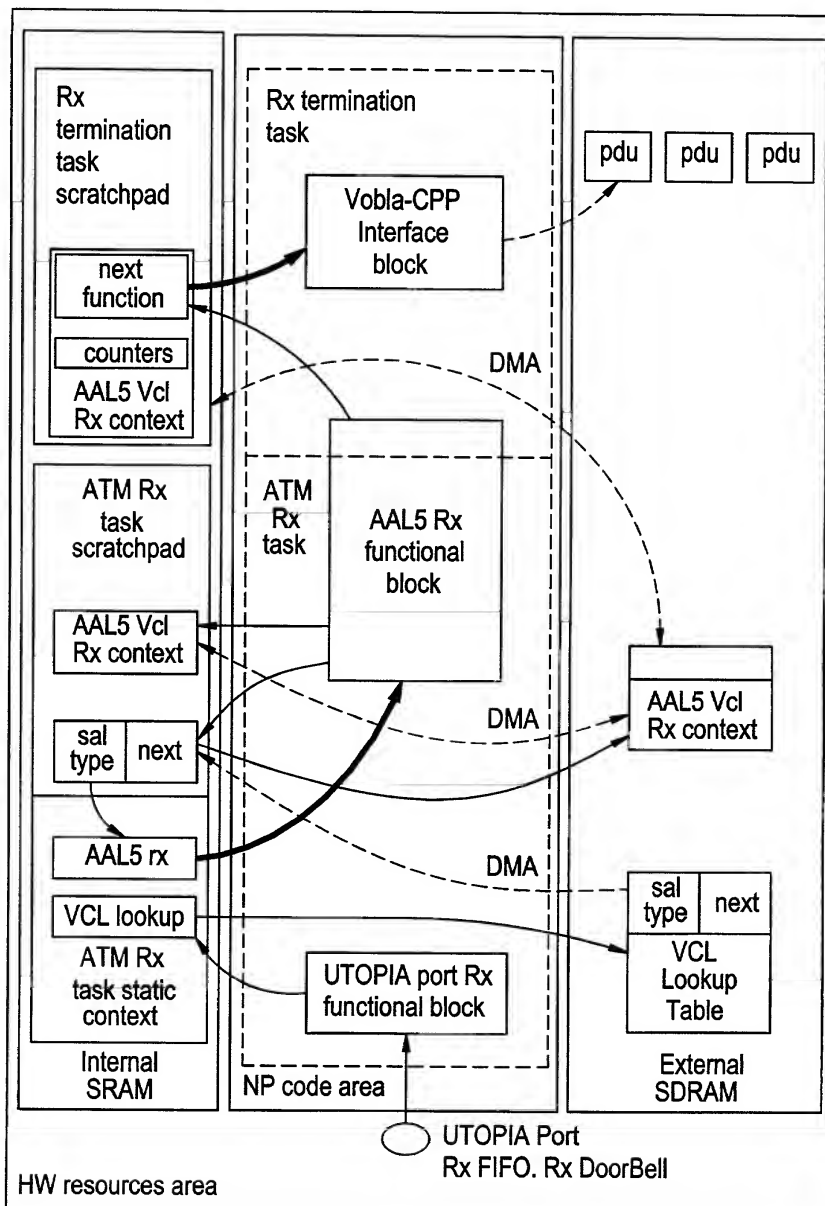


FIG. 84

1050



10064543.002402

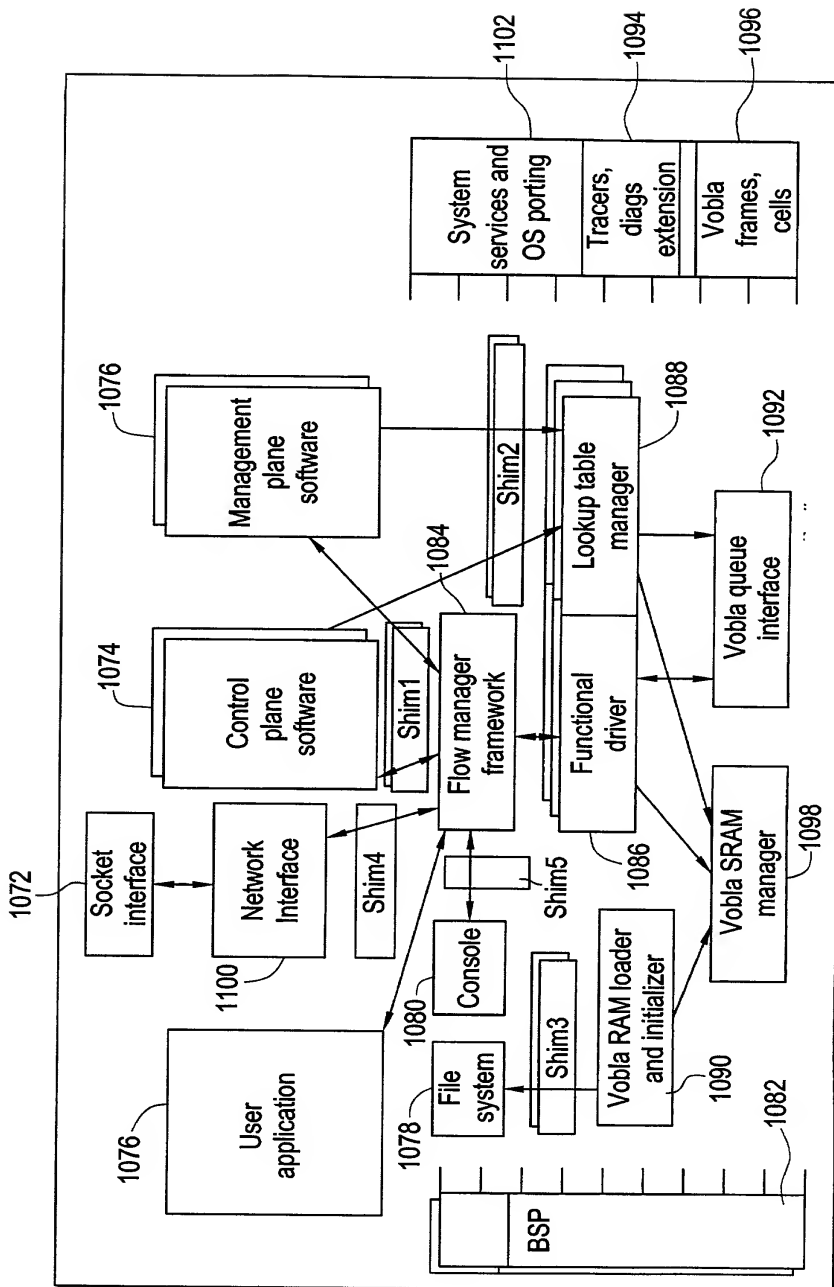


FIG. 86

1200

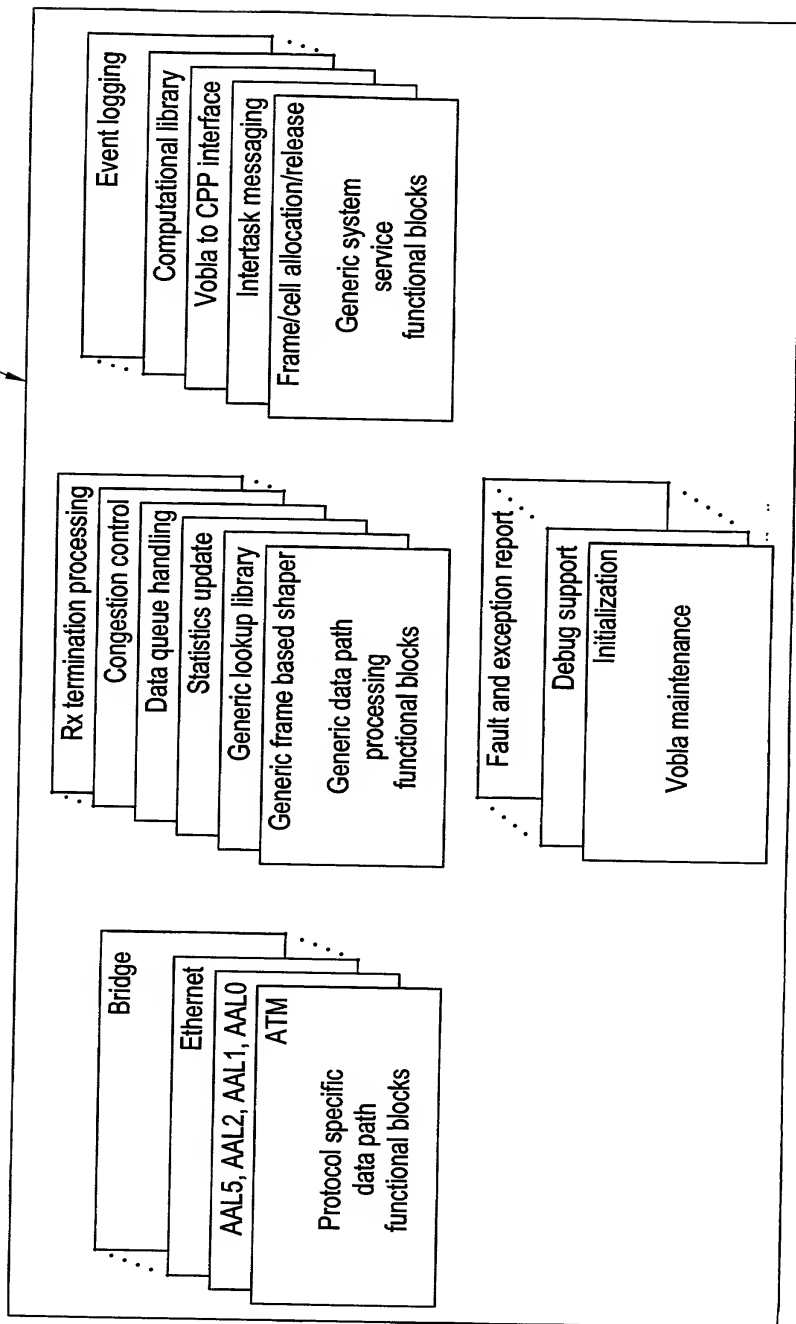
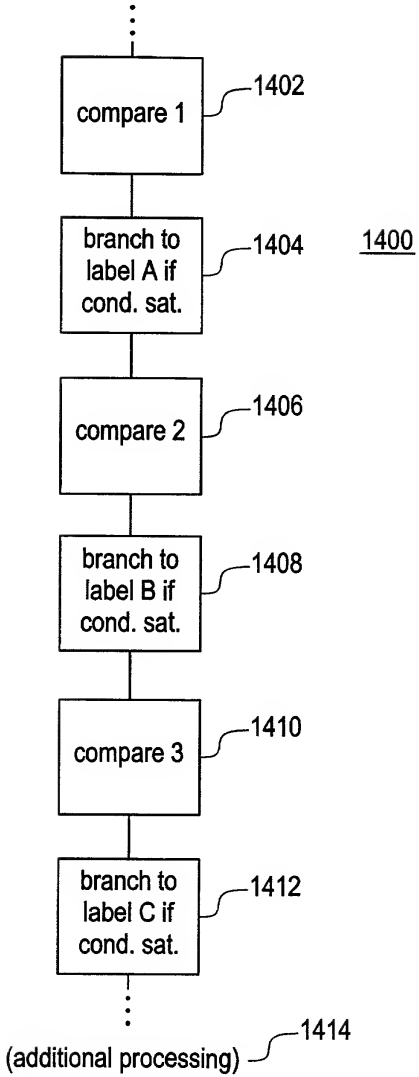




FIG. 87
PRIOR ART



10064313.002402

FIG. 88

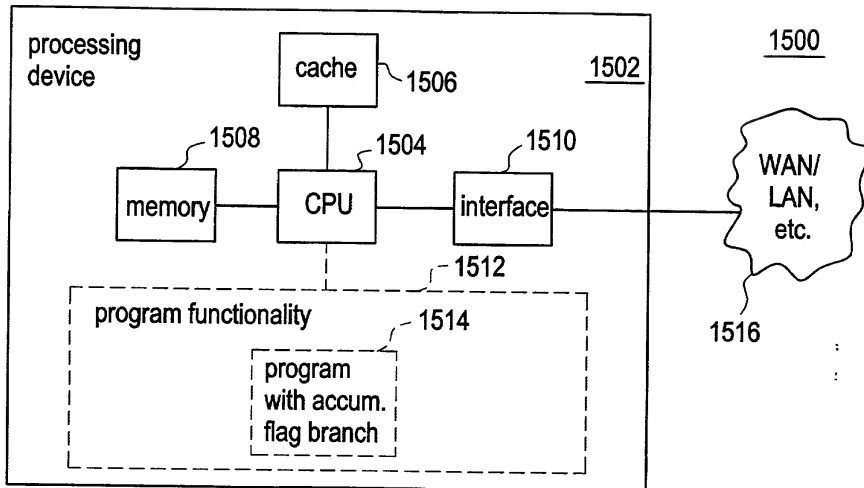


FIG. 89

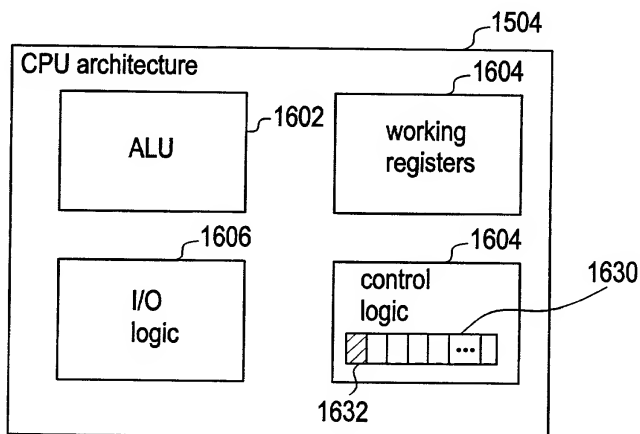


FIG. 90

